THOMSON DTH-8000

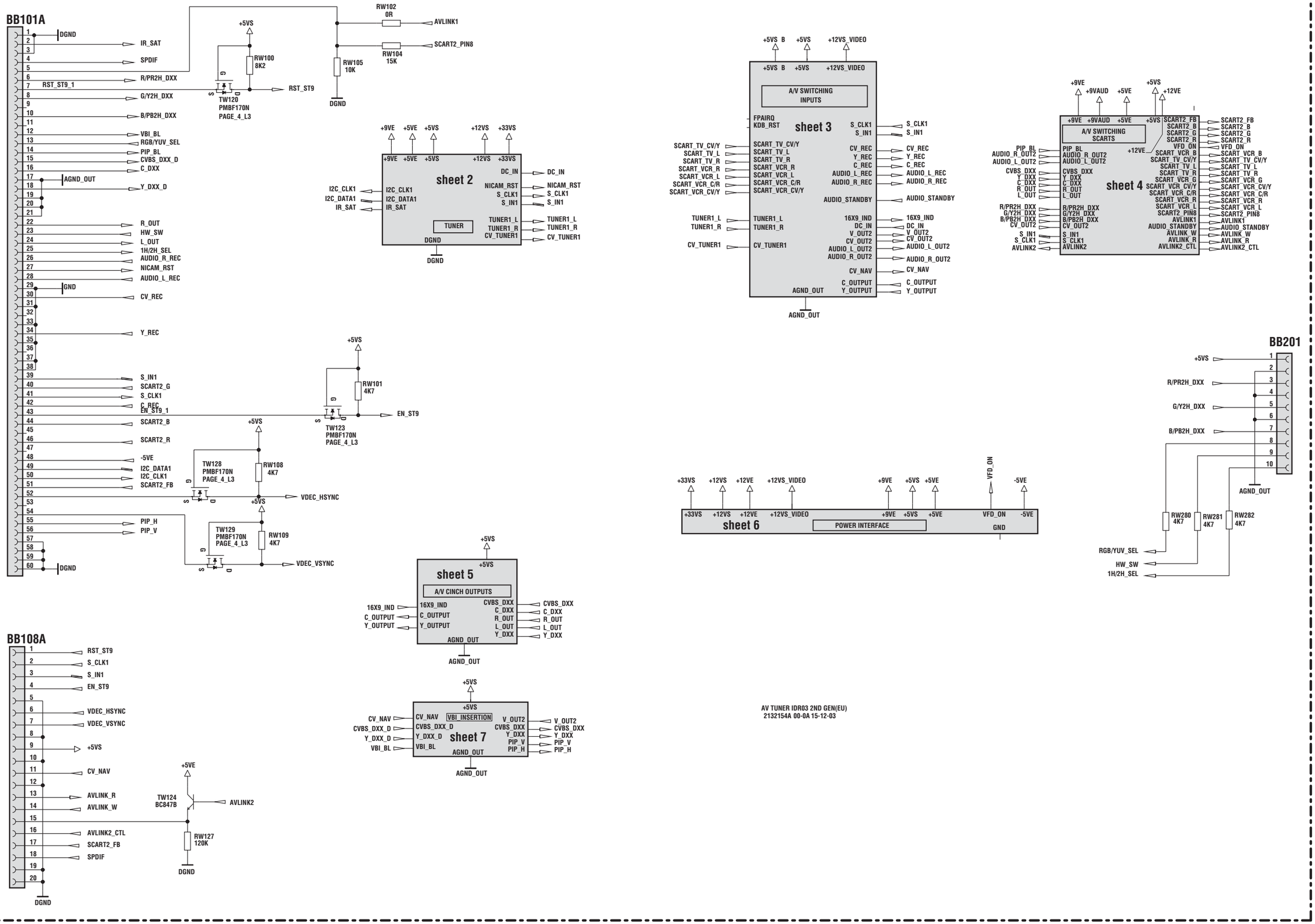
MODEL

SERVICE MANUAL

SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

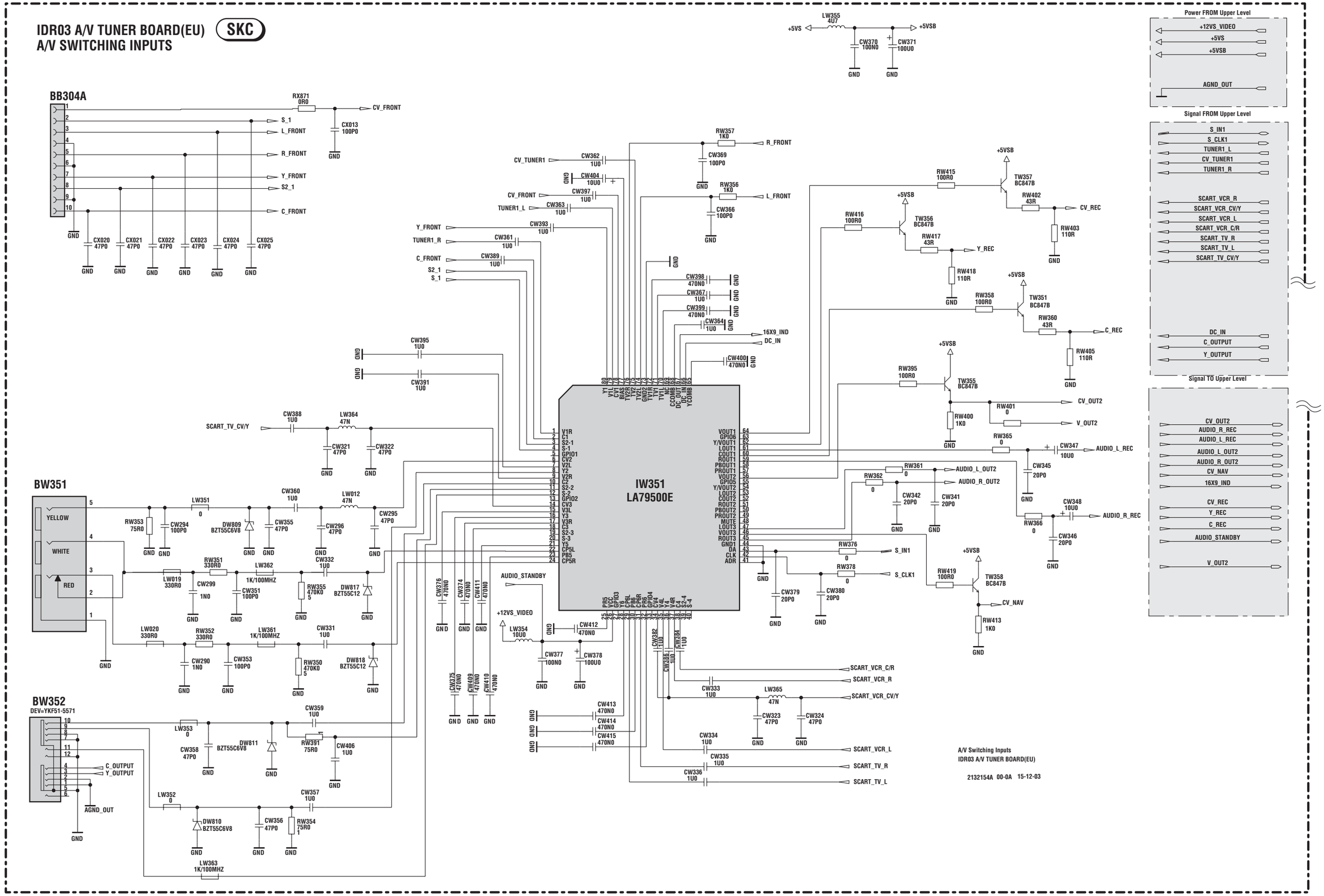
(AV TUNER BOARD 1/6)

AV TUNER IDR03 2ND GEN(EU) SKC



SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

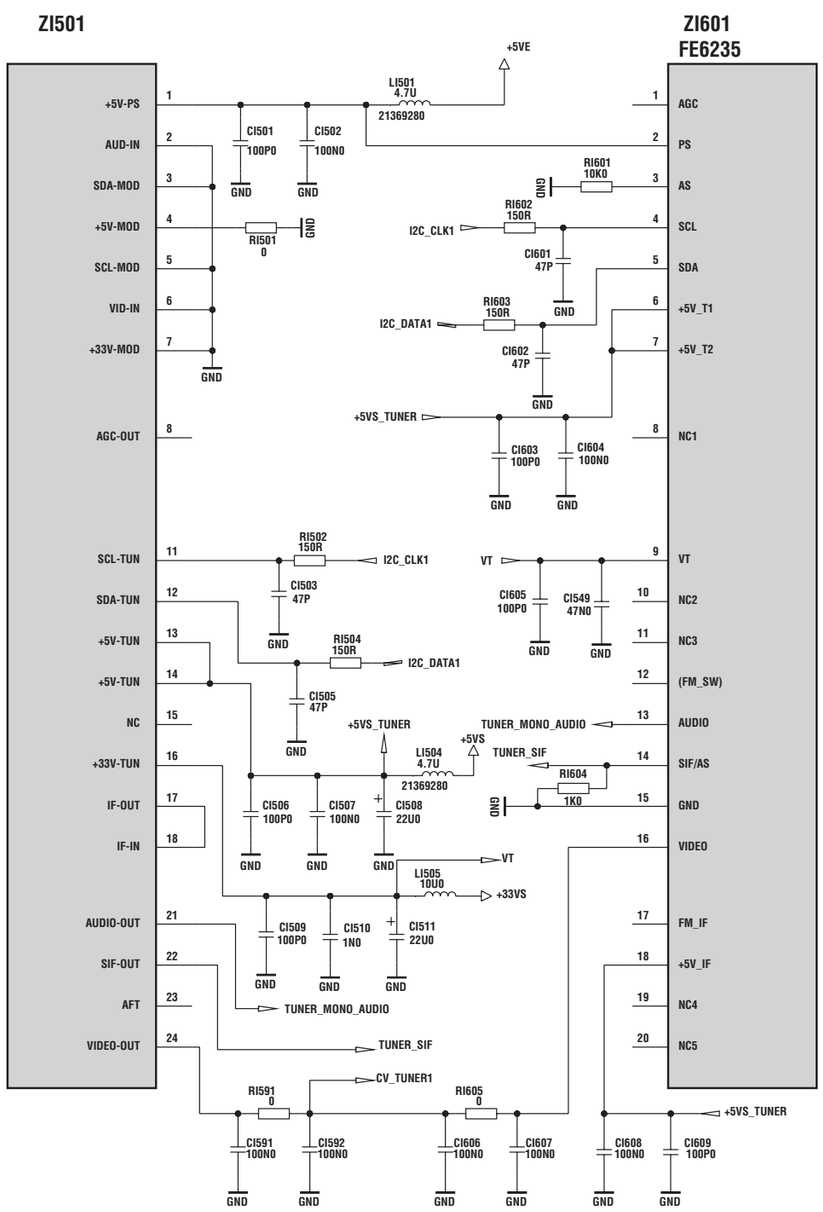
(AV TUNER BOARD 3/6)

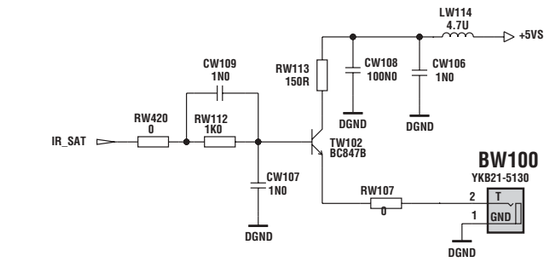


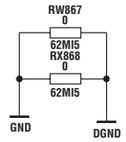
SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

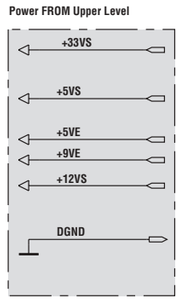
(AV TUNER BOARD 2/6)

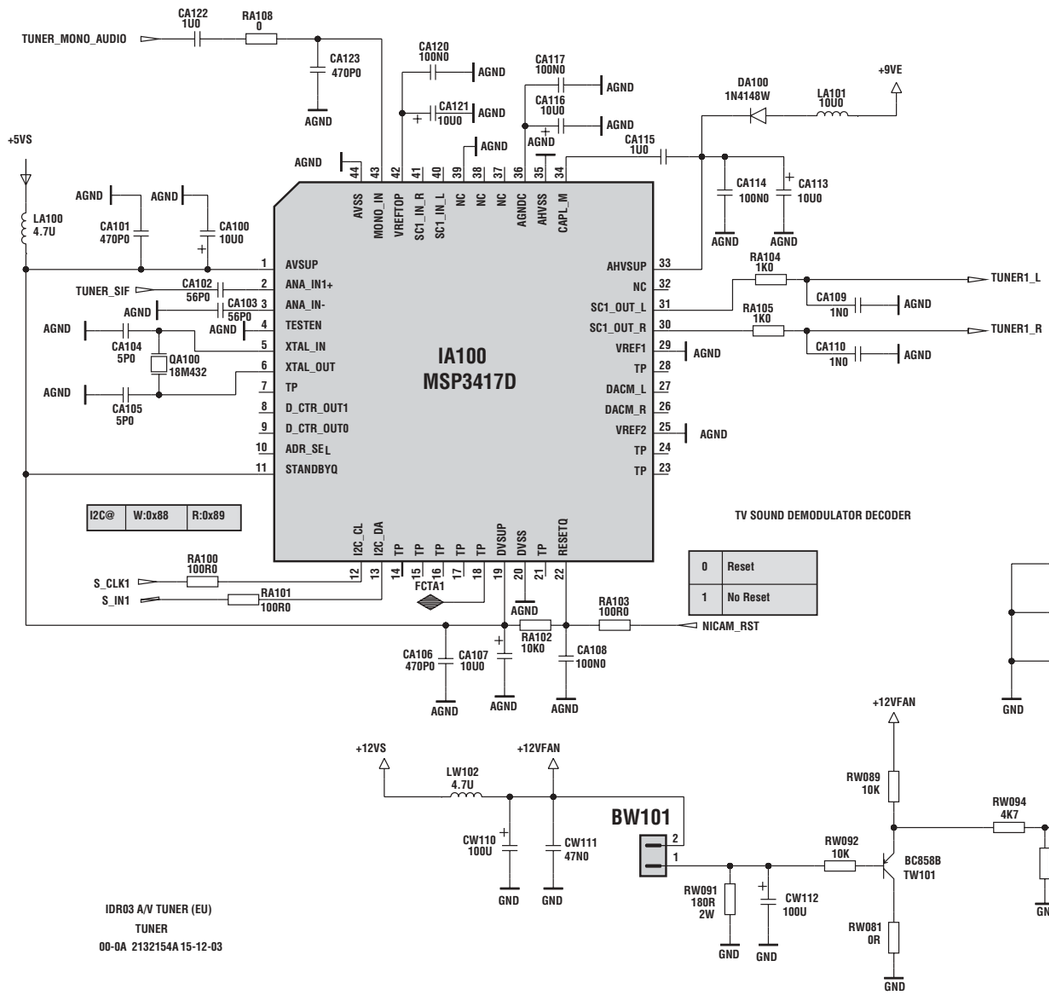
IDR03 A/V TUNER (EU) SKC
TUNER

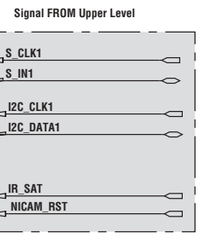


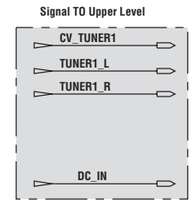


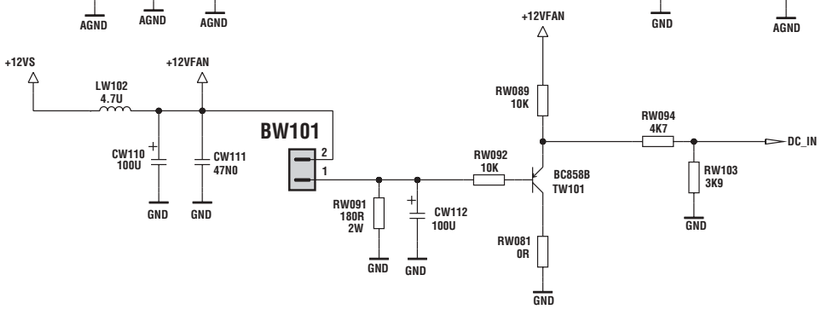








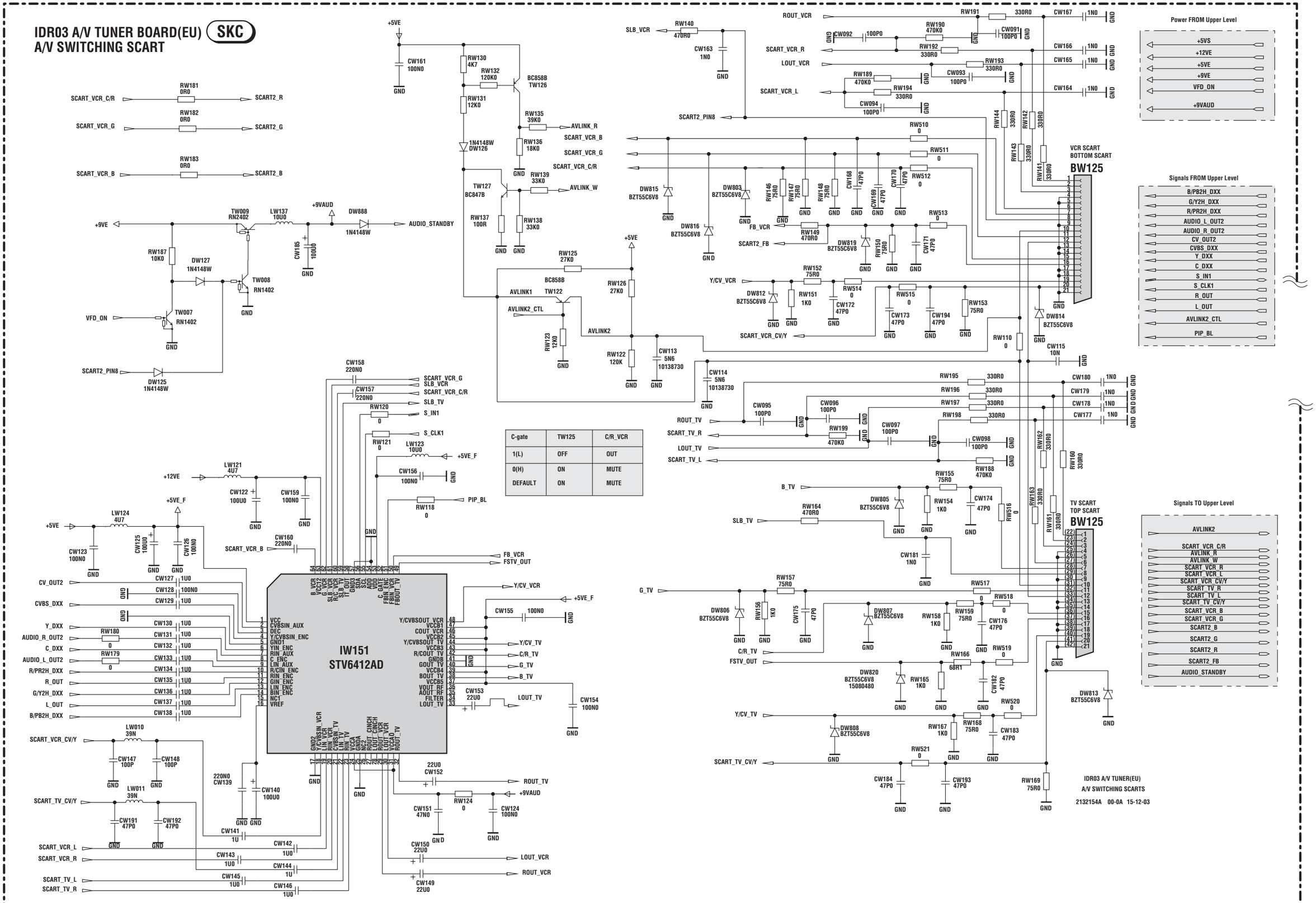




IDR03 A/V TUNER (EU)
TUNER
00-0A 2132154A 15-12-03

SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

(AV TUNER BOARD 4/6)



C-gate	TW125	C/R_VCR
1(L)	OFF	OUT
0(H)	ON	MUTE
DEFAULT	ON	MUTE

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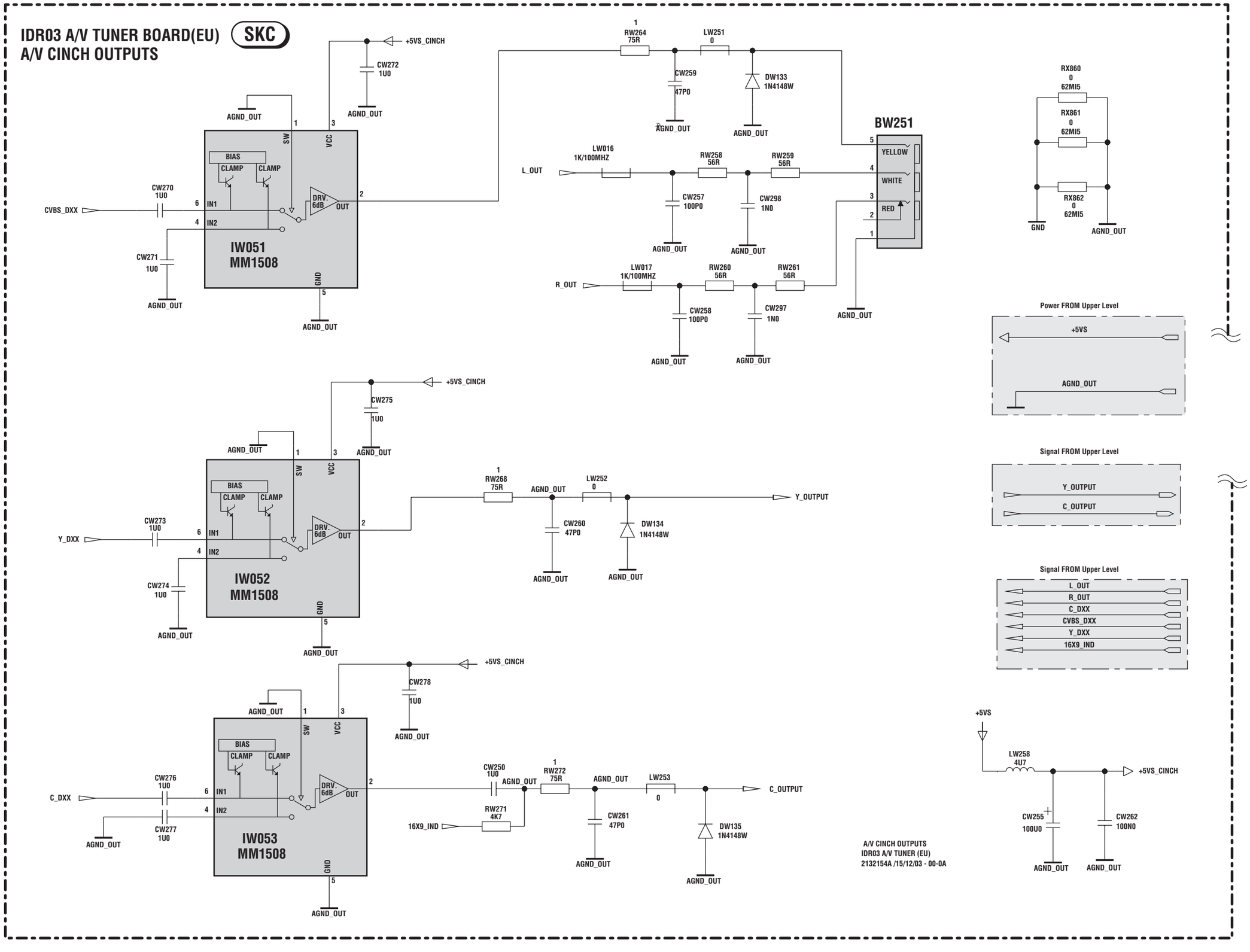
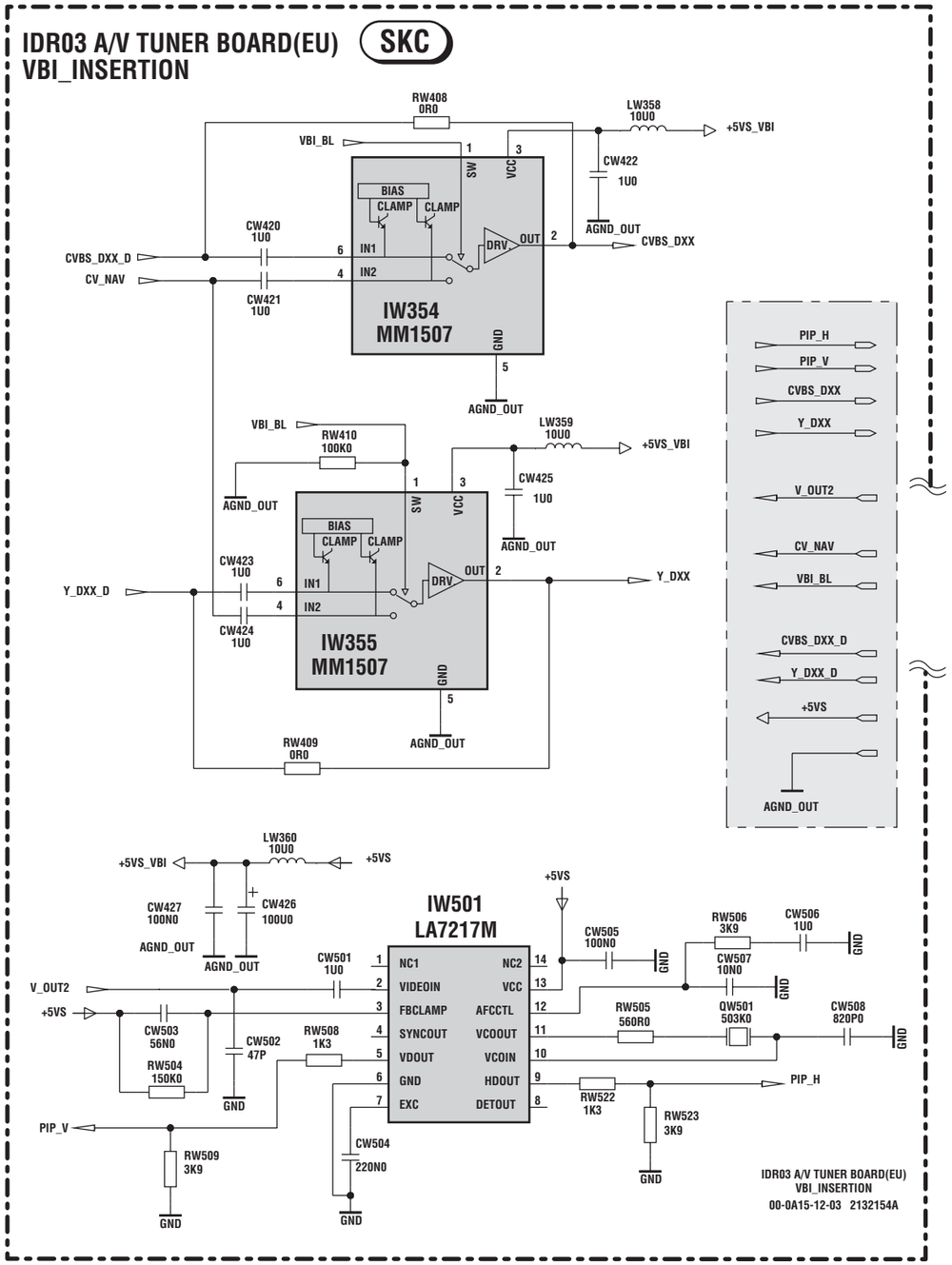
SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

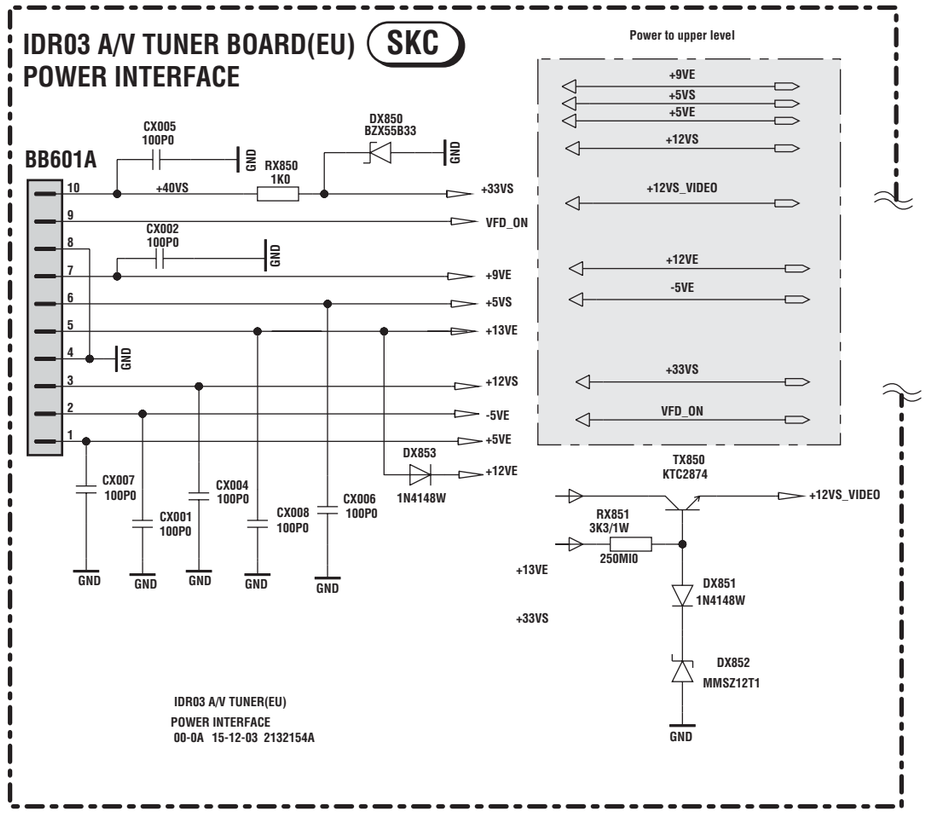
(AV TUNER BOARD 5/6)



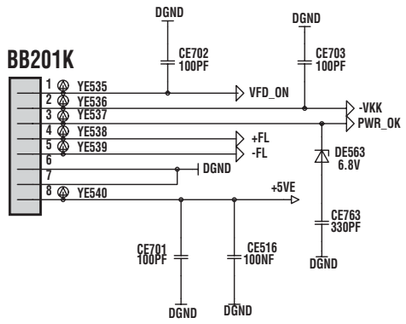
SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

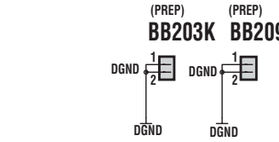
(AV TUNER BOARD 6/6)

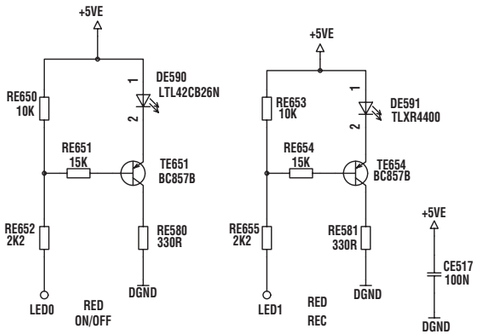


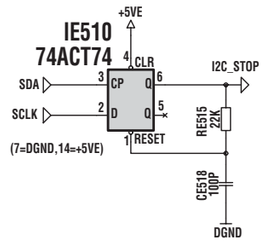


KDB_IDR03_(2ND_GEN) **KDB**



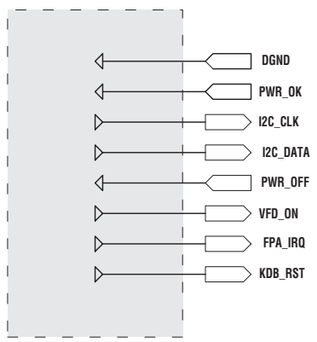




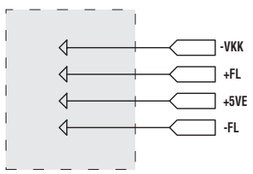


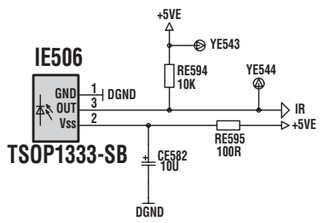
LED0	RED	ON/OFF
LED1	RED	RECORD

Signal to upper level

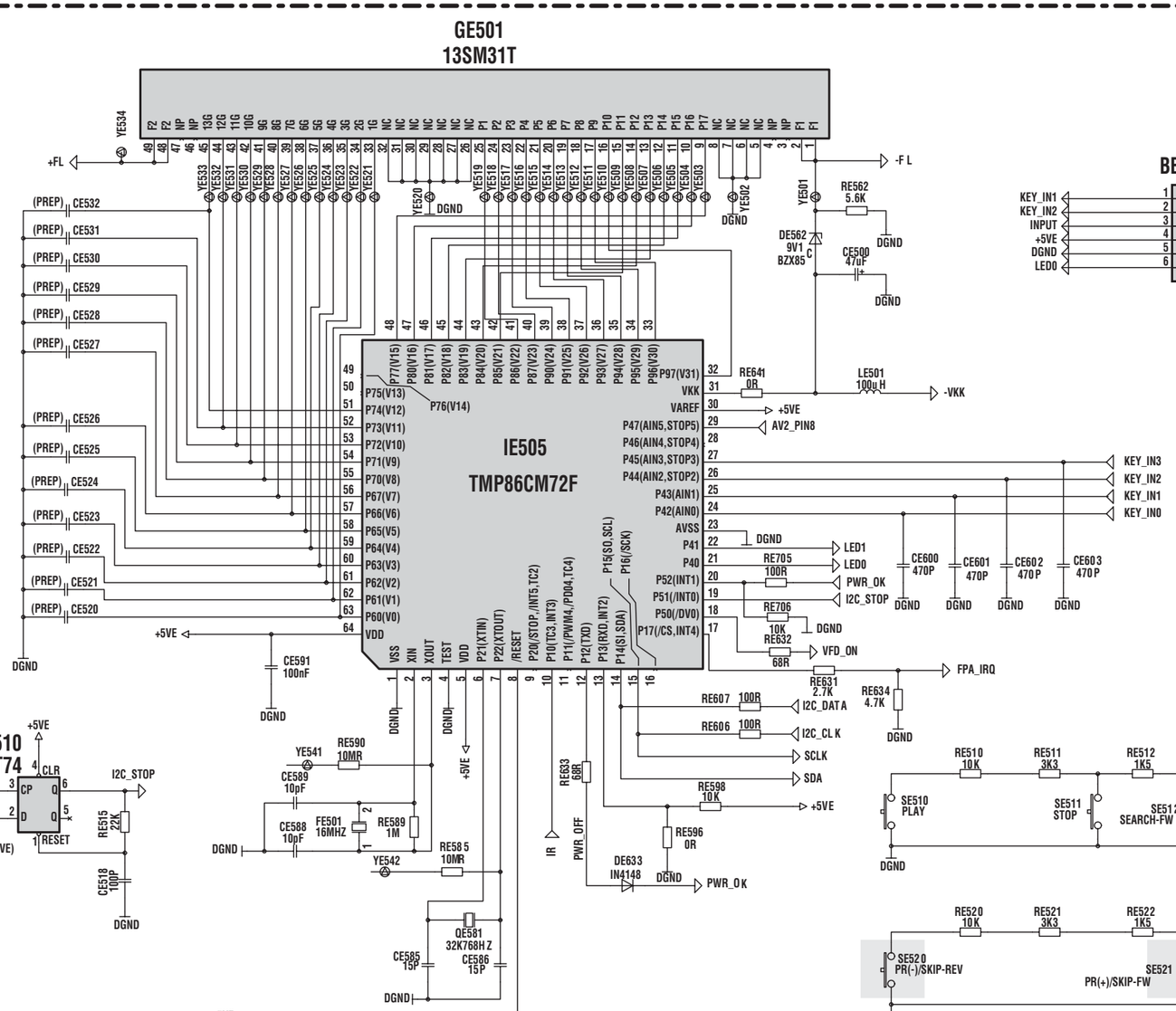


Power supply to upper level

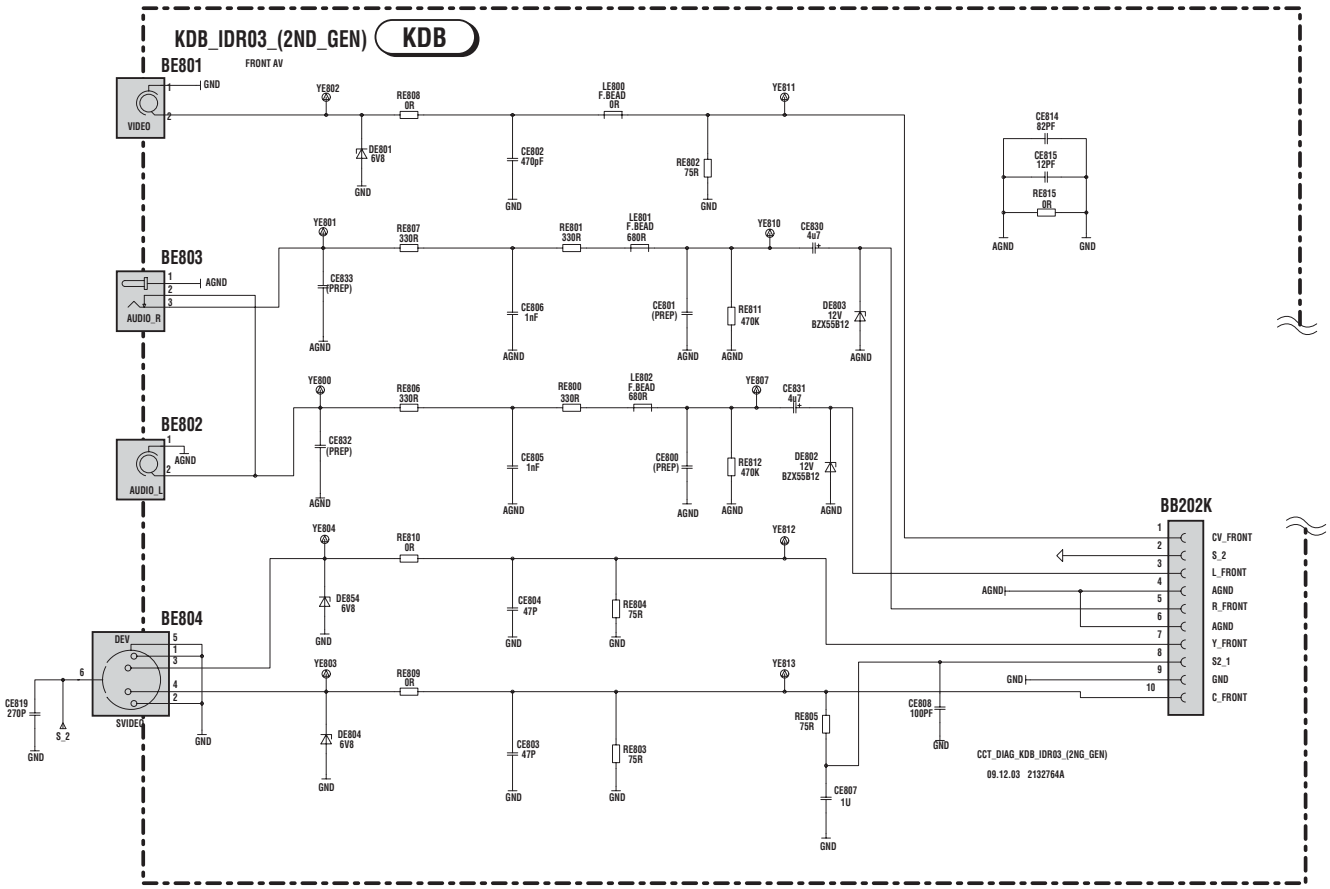




IR-RECEIVER		EU
TSOP1333-SB	33Khz	1

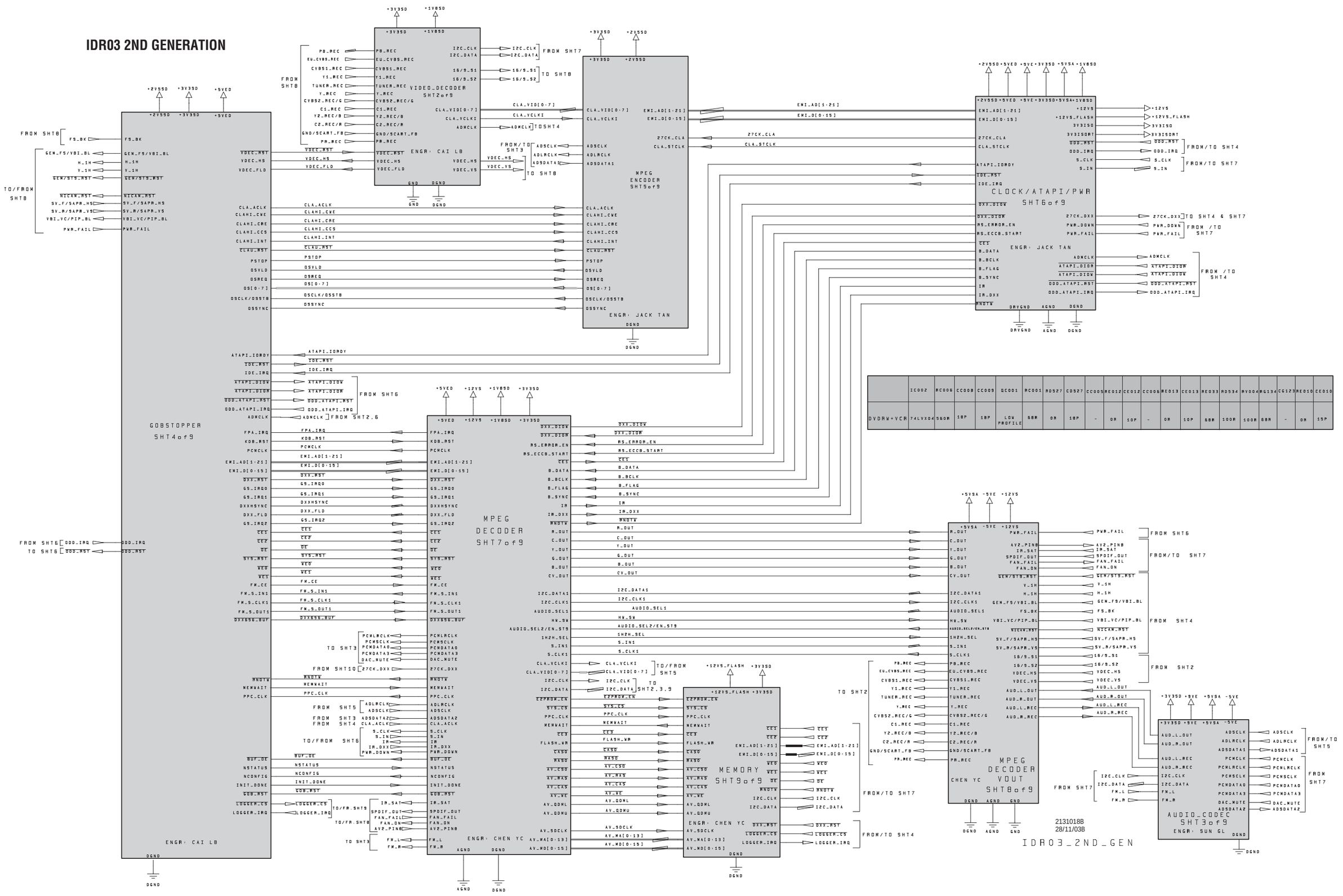


INPUT/OUTPUT - ENTREES/SORTIES - EINGÄNGE/AUSGÄNGE
INGRESSO/USCITA - ENTRADA/SALIDA



MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 1/9)



	IC002	RC006	CC008	CC009	QC001	RC001	RD527	CD527	CC005	RE012	CC012	CC006	RE013	CE013	RE033	RD534	RV004	RG134	CG123	RE010	CE010
DVDRW+VCR	74LVX04	560R	18P	18P	LOW PROFILE	68R	0R	18P	-	0R	10P	-	0R	10P	68R	100R	100R	68R	-	0R	15P

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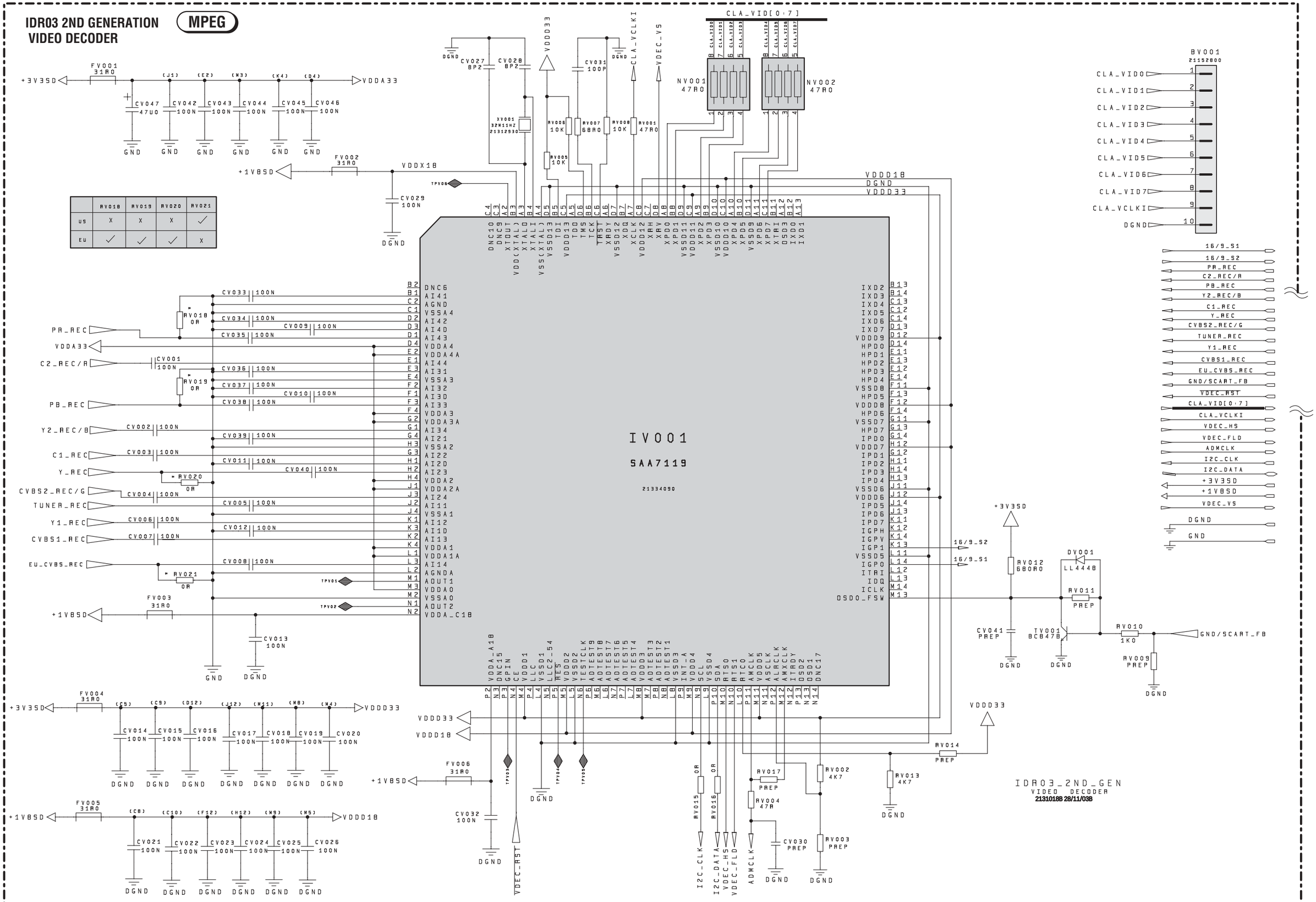
MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 2/9)



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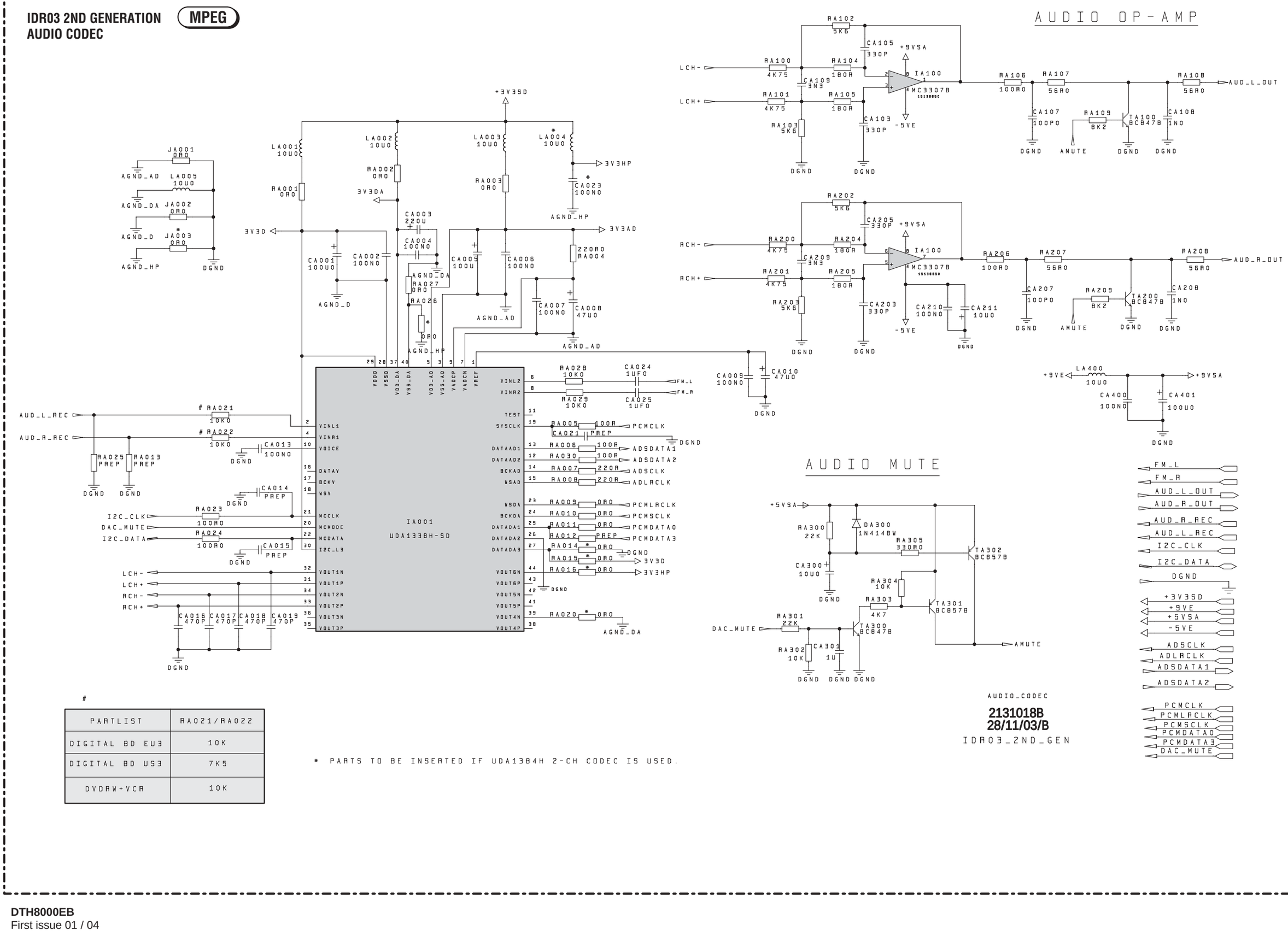
MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 3/9)

IDR03 2ND GENERATION
AUDIO CODEC

MPEG



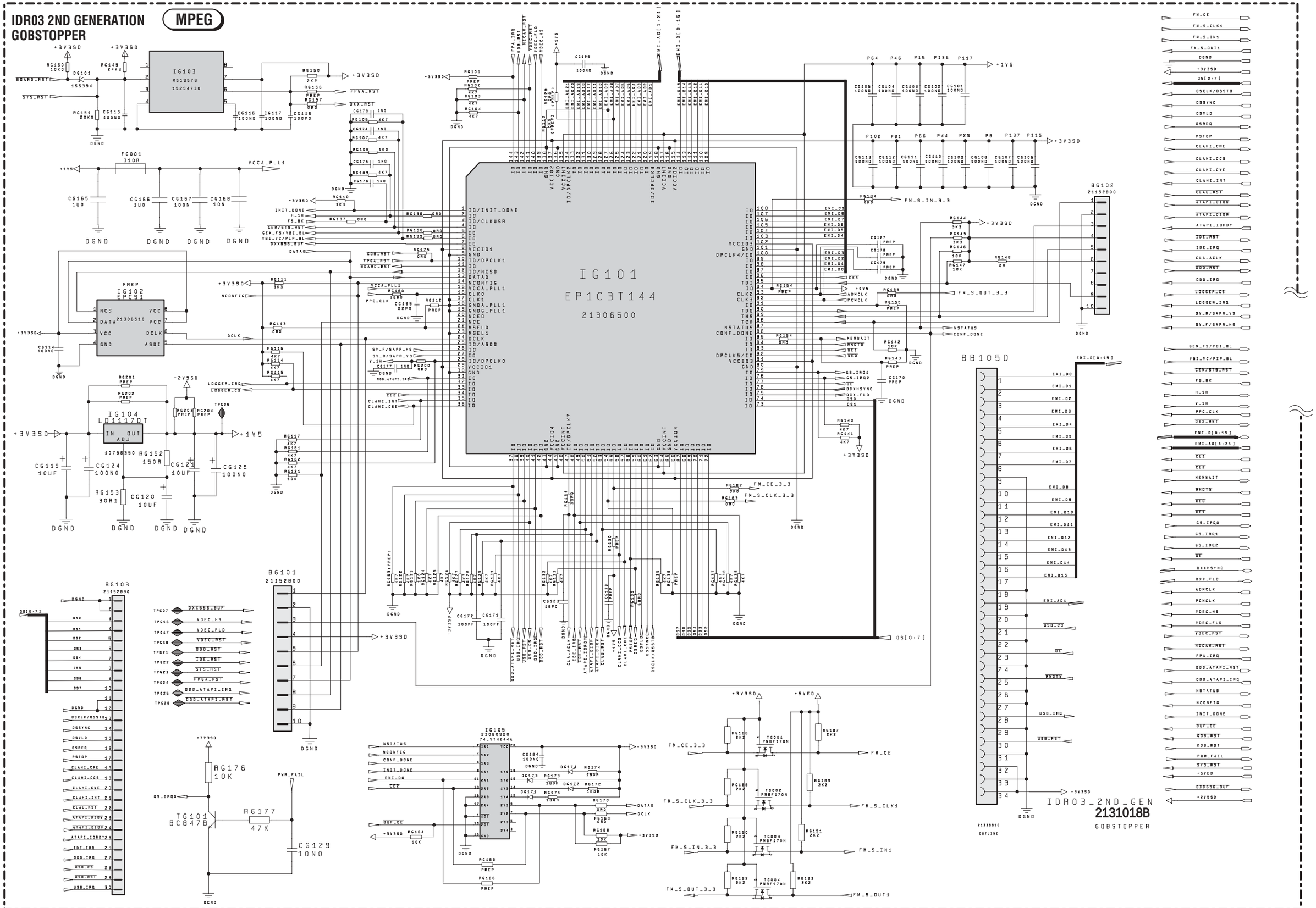
\#

PARTLIST	RA021/RA022
DIGITAL BD EU3	10K
DIGITAL BD US3	7K5
DVDRW+VCR	10K

* PARTS TO BE INSERTED IF UDA1384H 2-CH CODEC IS USED.

MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 4/9)



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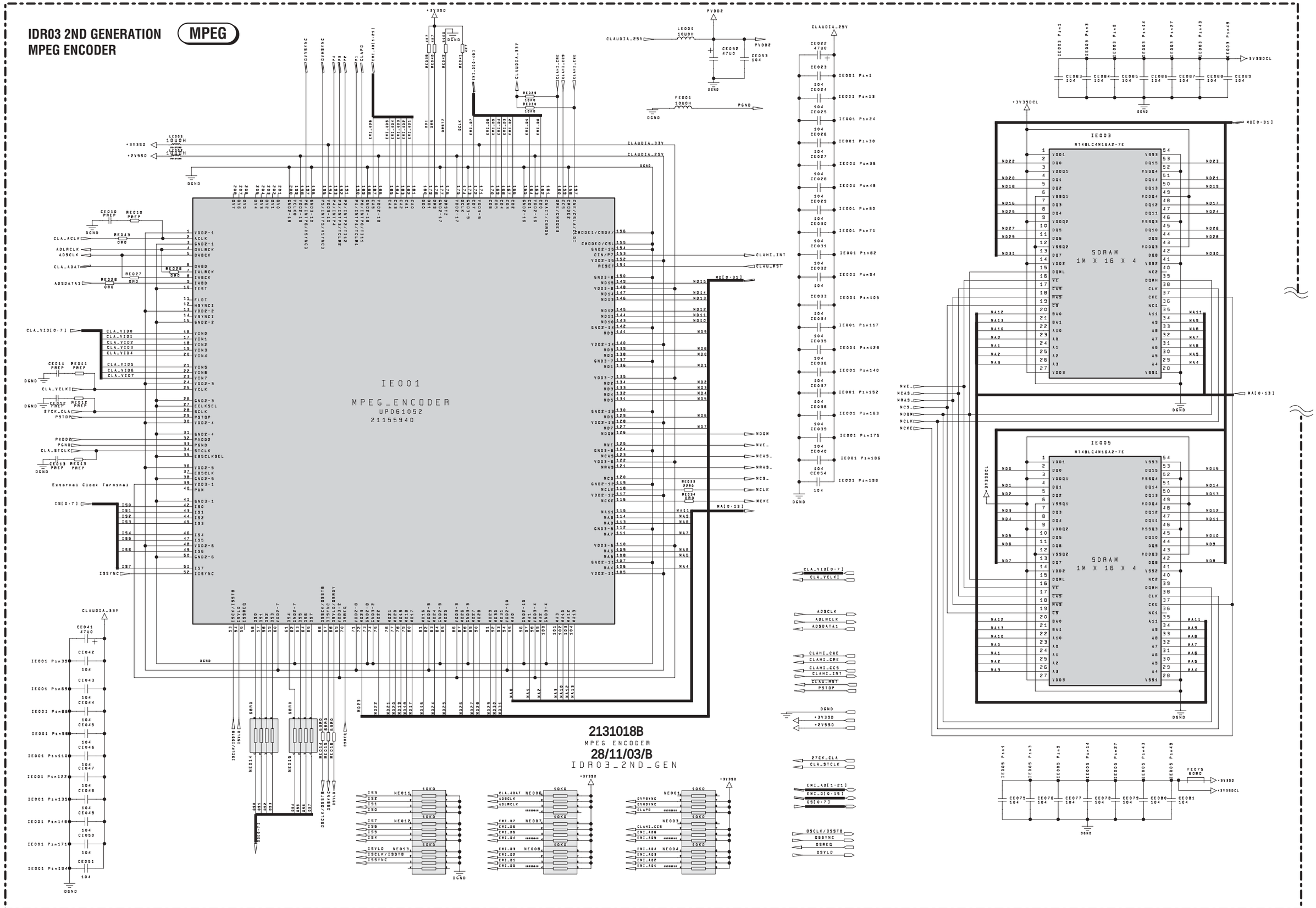
MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 5/9)



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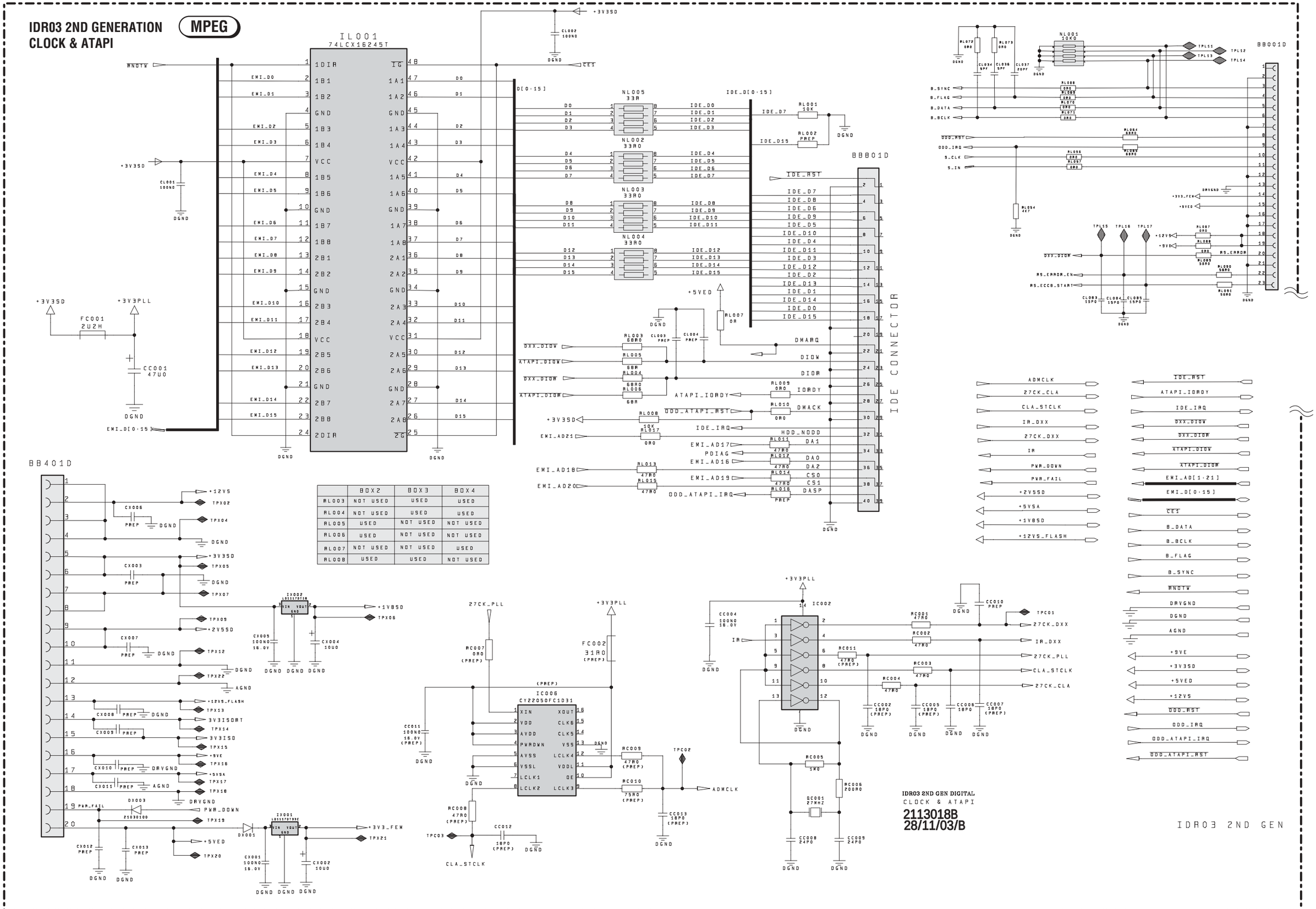
MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 6/9)



	BOX2	BOX3	BOX4
RL003	NOT USED	USED	USED
RL004	NOT USED	USED	USED
RL005	USED	NOT USED	NOT USED
RL006	USED	NOT USED	NOT USED
RL007	NOT USED	NOT USED	USED
RL008	USED	USED	NOT USED

MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 7/9)

MPEG

IDR03 2ND GENERATION MPEG DECCODER

	BB103D	BB104D	RD590	RD591	RD592	RD579	RD578	RD581	RD594	RD521	RD559
US	BWAY	X	X	X	X	✓	✓	✓	✓	X	✓
EURO	13WAY	✓	✓	✓	✓	X	X	X	X	✓	X

	RD595	RD561	RD575	RD543	RD545	RD547	RD542	RD544	RD546	RD548	RD549	RD596	RD519	RD520	RD512
US	X	X	✓	X	X	X	✓	X	✓	✓	X	✓	X	X	X
EURO	✓	✓	X	✓	✓	✓	X	X	X	X	✓	X	✓	✓	✓

2131018B
IDR03 2ND GEN DIGITAL
MPEG DECODER

IDR03_2ND_GEN

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MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 8/9)

IDR03 2ND GENERATION MPEG DECODER VOUT

MPEG

	RD360	RD361	RD362	RD365	RD366	RD367	RD368	RD369	RD330	RD340	RD350
US	X	✓	X	✓	X	✓	✓	X	174R	174R	174R
EURO	✓	X	✓	X	✓	X	X	✓	200R	200R	200R

BB101D

IR_SAT, SPDIF_OUT, R_PR2H_DXX, GEM/ST9_RST, G_Y2H_DXX, V_1H, B_PB2H_DXX, H_1H, GEM_FS/VBI_BL, FS_BK, VBI_VC/PIP_BL, CVBS_DXX, C_DXX, Y_DXX, AUD_R_OUT, HW_SW, AUD_L_OUT, 1H2H_SEL, AUD_R_REC, NICAM_RST, AUD_L_REC, CVBS1_REC, Y1_REC, TUNER_REC, CVBS2_REC/G, C1_REC, AUDIO_SEL2/EN_ST9, Y2_REC/B, FAN_FAIL, C2_REC/R, FAN_ON, -5VE, I2C_DATA1, I2C_CLK1, GND/SCART_FB, VDEC_HS, PR_REC, VDEC_VS, SV_F/SAPR_HS, SV_R/SAPR_VS, 16/9_S1, 16/9_S2

RD368 0R0 → PWR_FAIL; RD369 0R0 → AV2_PIN8; RD365 0R0 → PB_REC; RD366 0R0 → EU_CVBS_REC; RD367 0R0 → Y_REC; RD360 0R0 → S_IN1; RD361 0R0 → AUDIO_SEL1; RD362 0R0 → S_CLK1

AGND, DGND, GND

+5VAV

RD301 1K0 → TPD66 → CVBS_DXX; LD300 4U7H; TD300 BC857B; CD300 100P0; CD301 100P0; RD300 174R0

RD311 1K0 → TPD67 → Y_DXX; LD310 4U7H; TD310 BC857B; CD310 100P0; CD311 100P0; RD310 174R0

RD321 1K0 → TPD68 → C_DXX; LD320 4U7H; TD320 BC857B; CD320 100P0; CD321 100P0; RD320 174R0

RD331 1K0 → TPD69 → R_PR2H_DXX; LD330 2U2H; TD330 BC857B; CD330 39P0; CD331 39P0; RD330 174R0

RD341 1K0 → TPD70 → G_Y2H_DXX; LD340 2U2H; TD340 BC857B; CD340 39P0; CD341 39P0; RD340 174R0

RD351 1K0 → TPD71 → B_PB2H_DXX; LD350 2U2H; TD350 BC857B; CD350 39P0; CD351 39P0; RD350 174R0

GND, AGND, DGND, +12VS, +5VSA, CV_OUT, Y_OUT, C_OUT, R_OUT, G_OUT, B_OUT

+5VSA → FD300 330R → +5VAV; CD312 100N; CD313 47U; RD363 0R0; RD364 0R0

MPEG DECODER VOUT
IDR03 2ND GEN DIGITAL
2131018B
28/11/03/B

IDR03_ 2ND_ GEN

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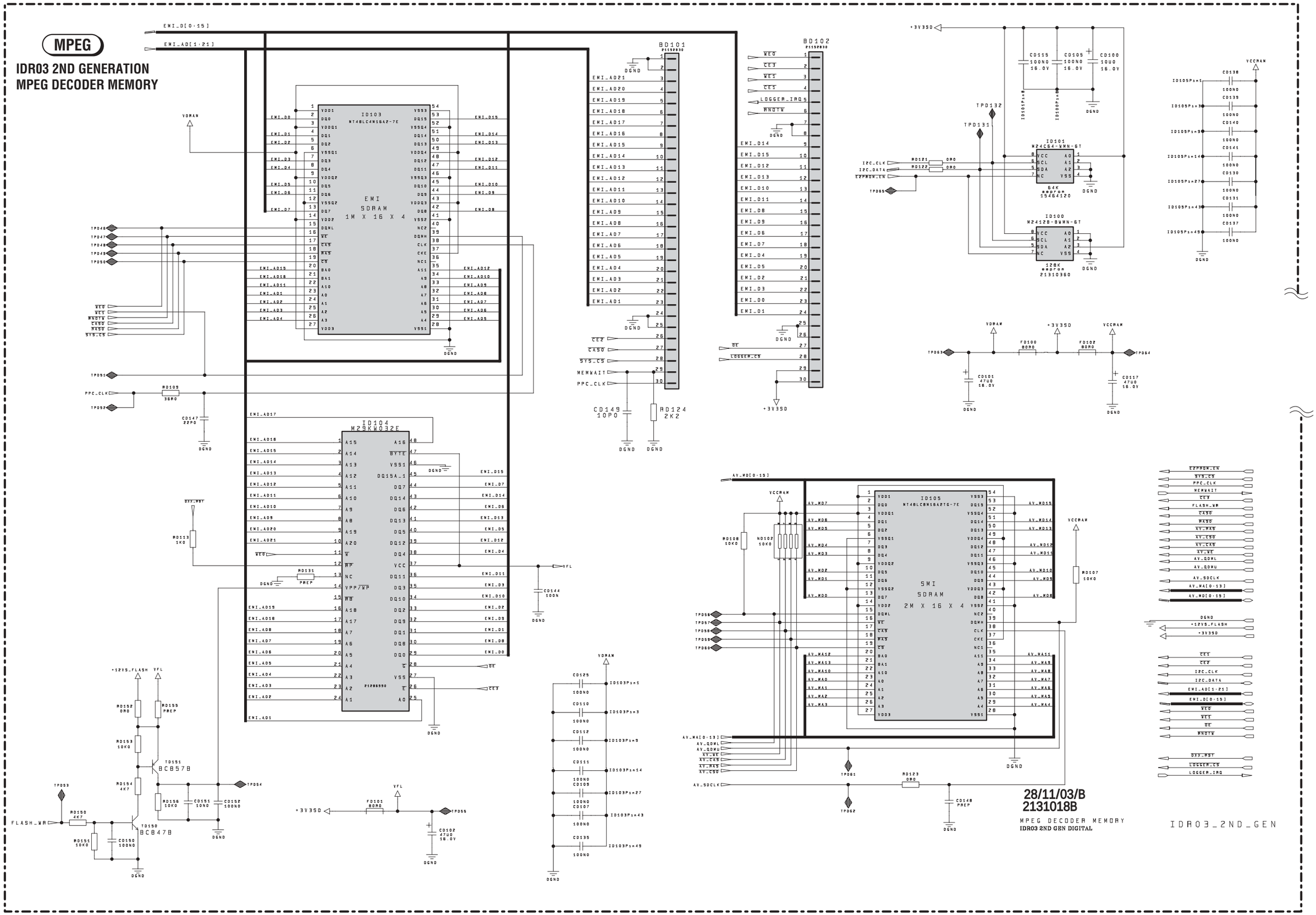
MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 9/9)



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IDR03 2ND GENERATION
USB

USB

BB105U

EMI_D[0:15]

EMI_D0 EMI_D1 EMI_D2 EMI_D3 EMI_D4 EMI_D5 EMI_D6 EMI_D7 EMI_D8 EMI_D9 EMI_D10 EMI_D11 EMI_D12 EMI_D13 EMI_D14 EMI_D15

EMI_AD1

USB_CS

OE

RNOTW

USB_IRQ

USB_RST

CE811 12P0F (PREP)

DE804 BZT55C6V8 (PREP)

DE805 BZT55C6V8 (PREP)

DGND

+3V3SD

CE019 100P0F

CE018 1N0F

RE925 0R0

DGND

IE833
ISP1160

D1 D0 DGND7 NC8 TEST_LOW5 A0 VREG/3.3 AGND VCC H_OC2 H_OC1 H_DP2 H_DM2 H_DP1 H_DM1 TEST_LOW4

DGND1 D2 D3 D4 D5 D6 D7 DGND2 D8 D9 D10 D11 D12 D13 DGND3 D14

D15 DGND4 VHOLD1 NC1 CS RD WR VHOLD2 DREQ NC2 DACK TEST_HIGH INT NC3 NC4 RESET

TEST_LOW3 H_PSW2 H_PSW1 DGND6 XTAL2 XTAL1 H_SUSPEND NC7 H_WAKEUP TEST_LOW2 NC6 TEST_LOW1 NC5 DGND5 EOT NDP_SEL

+3V3SD

RE911 22R0

RE910 22R0

RE909 0R0

RE908 0R0

CE809 100NO

CE808 100NO

RE914 10K0

RE917 4K7

RE913 10K0

RE906 15K0

CE805 47P0F

RE907 15K0

CE804 47P0F

CE810 100NO

DGND

FE800 0Ω

DE800 BZT55C6V8

DE801 BZT55C6V8

CE806 220U0F

CE816 100P0F

CE817 1N0F

DE802 BZT55C6V8

BB800U

GND USB+ USB- VBUS SHIELD

BB810U

GND USB+ USB- VBUS SHIELD

CE803 100NO

RE905 10K0

+5VE

IE800
USB10H

G1 S2 G2 D1 S1 D2

P-Channel MOSFET

RE904 10K0

RE903 10K0

RE902 10K0

RE901 10K0

RE912 4K7

RE900 10K0

XB800 6MHZ

CE800 18P0F

CE801 18P0F

DGND

CE802 100NO

CE807 100NO

RE915 4K7

RE916 4K7

RE923 0R0

RE924 0R0

USB_CS OE RNOTW DACK USB_IRQ USB_RST

LE800 2U2H

BE800

BB701U

CE814 100P0F

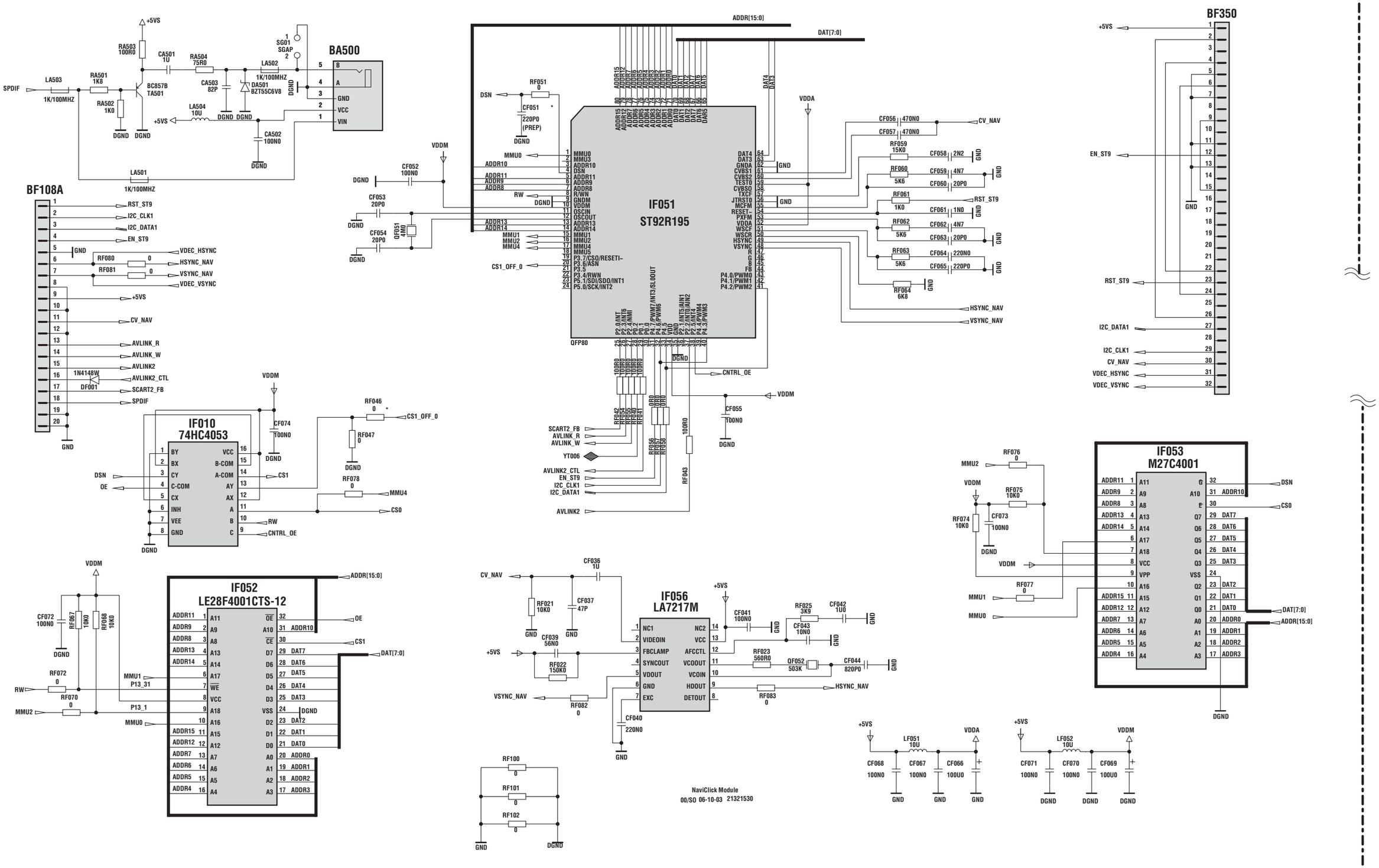
CE815 1N0F

DE803 BZT55C6V8 (PREP)

RE919 0R0

USB 2nd GEN
2131495A-0A-SO
0A-SO 09.12.03

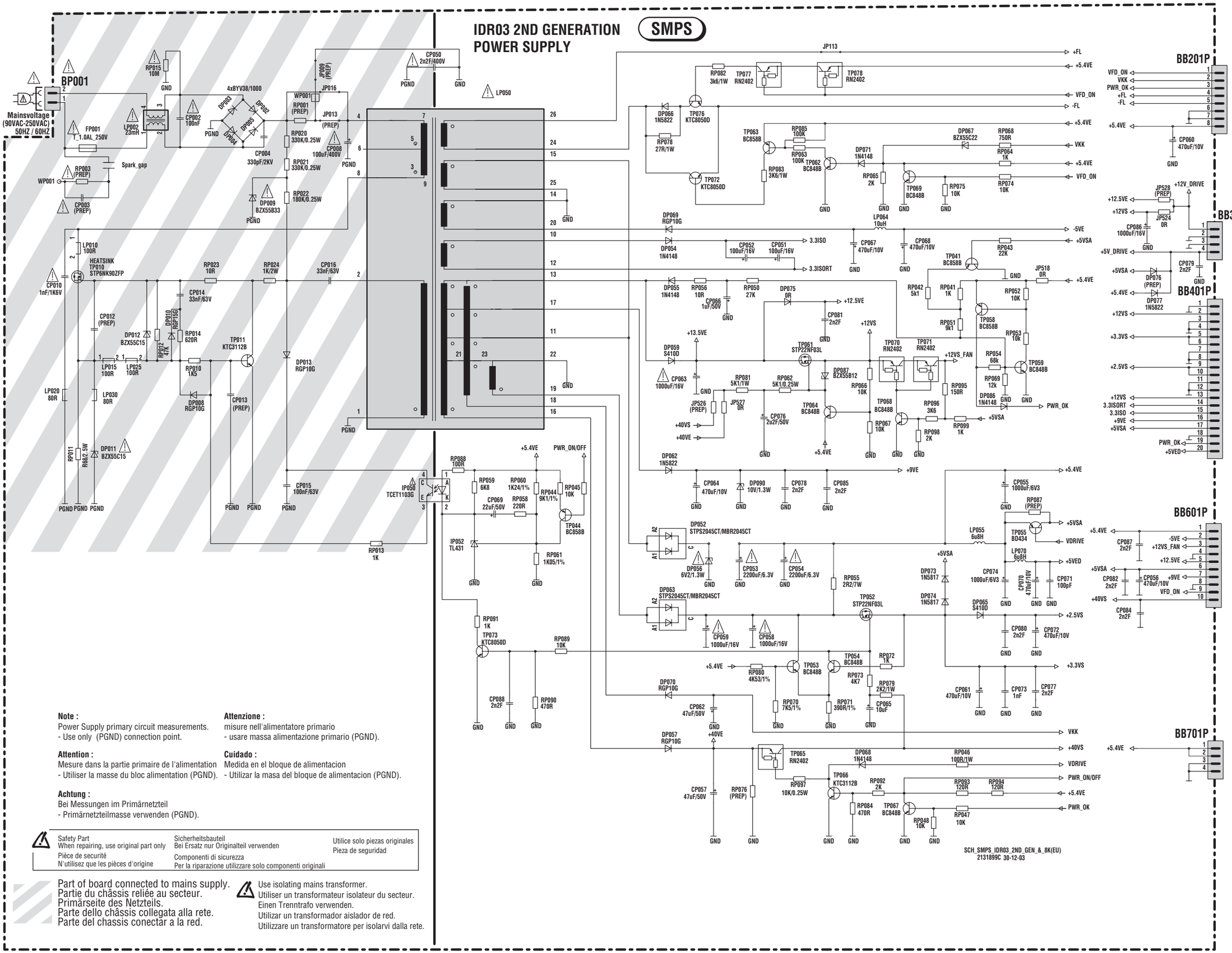
NAVICLICK MODULE ONSC



DTH8000EB
First issue 01 / 04

POWER SUPPLY INTERFACE - INTERFACE ALIMENTATION - NETZTEIL - ALIMENTAZIONE - INTERFAZ ALIMENTACIÓN

SCHEMATIC DIAGRAM - SCHEMA DE PRINCIPE - SCHALTBILD - SCHEMA - ESQUEMA



Note :
Power Supply primary circuit measurements.
- Use only (PGND) connection point.

Attention :
Mesure dans la partie primaire de l'alimentation
- Utiliser la masse du bloc alimentation (PGND).

Achtung :
Bei Messungen im Primärnetzteil
- Primärnetzteilmasse verwenden (PGND).

Attenzione :
misure nell'alimentatore primario
- usare massa alimentazione primario (PGND).

Cuidado :
Medida en el bloque de alimentacion
- Utilizar la masa del bloque de alimentacion (PGND).

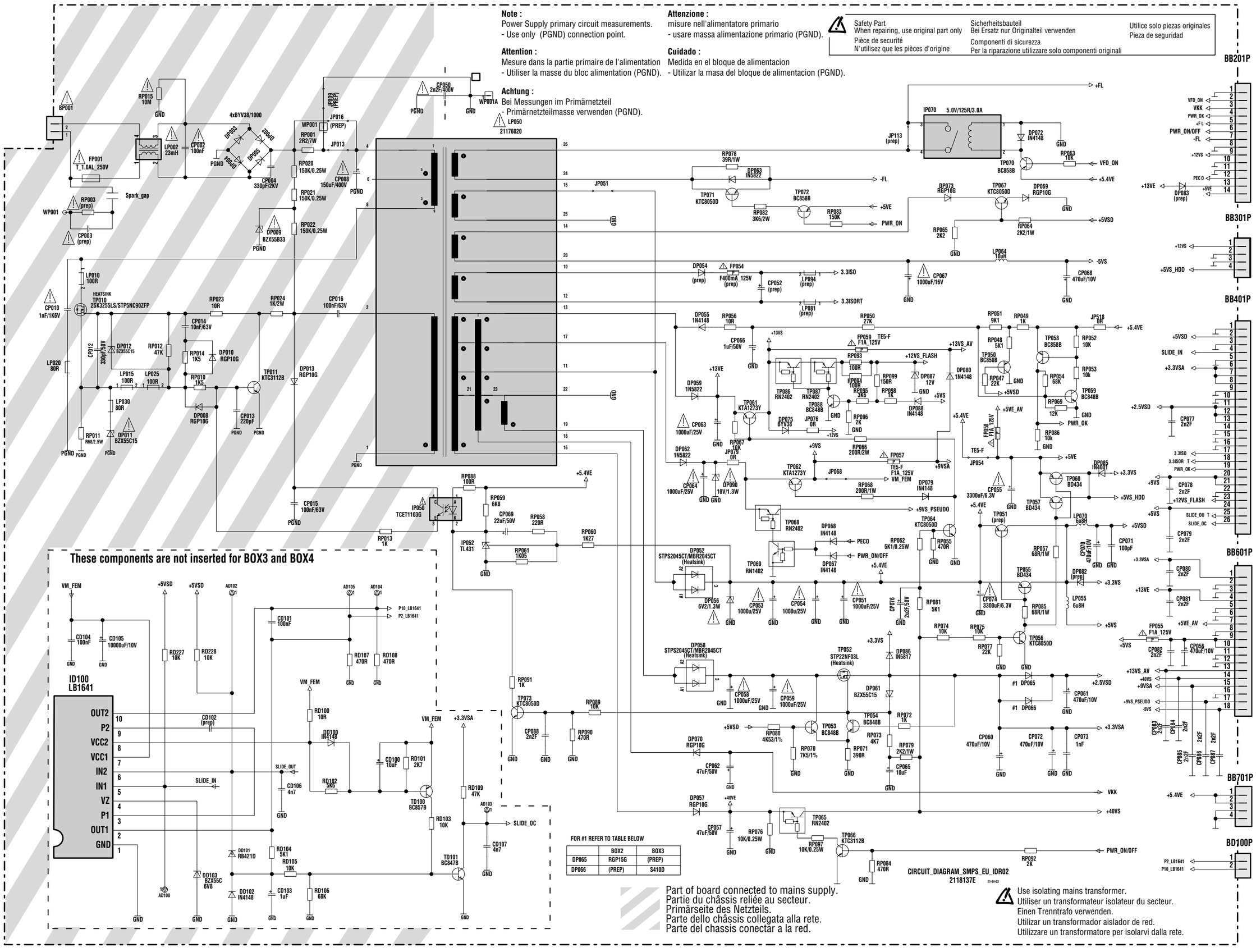
Safety Part When repairing, use original part only	Sicherheitsbauteil Bei Ersatz nur Originalteil verwenden	Utilice solo piezas originales
Pièce de securité N'utilisez que les pièces d'origine	Componenti di sicurezza Per la riparazione utilizzare solo componenti originali	Pieza de seguridad

Part of board connected to mains supply.
Partie du châssis reliée au secteur.
Primärseite des Netzteils.
Parte dello châssis collegata alla rete.
Parte del chassis conectar a la red.

Use isolating mains transformer.
Utiliser un transformateur isolateur du secteur.
Einen Trenntrafo verwenden.
Utilizar un transformador aislador de red.
Utilizzare un transformatore per isolarvi dalla rete.

POWER SUPPLY INTERFACE - INTERFACE ALIMENTATION - NETZTEIL - ALIMENTAZIONE - INTERFAZ ALIMENTACIÓN

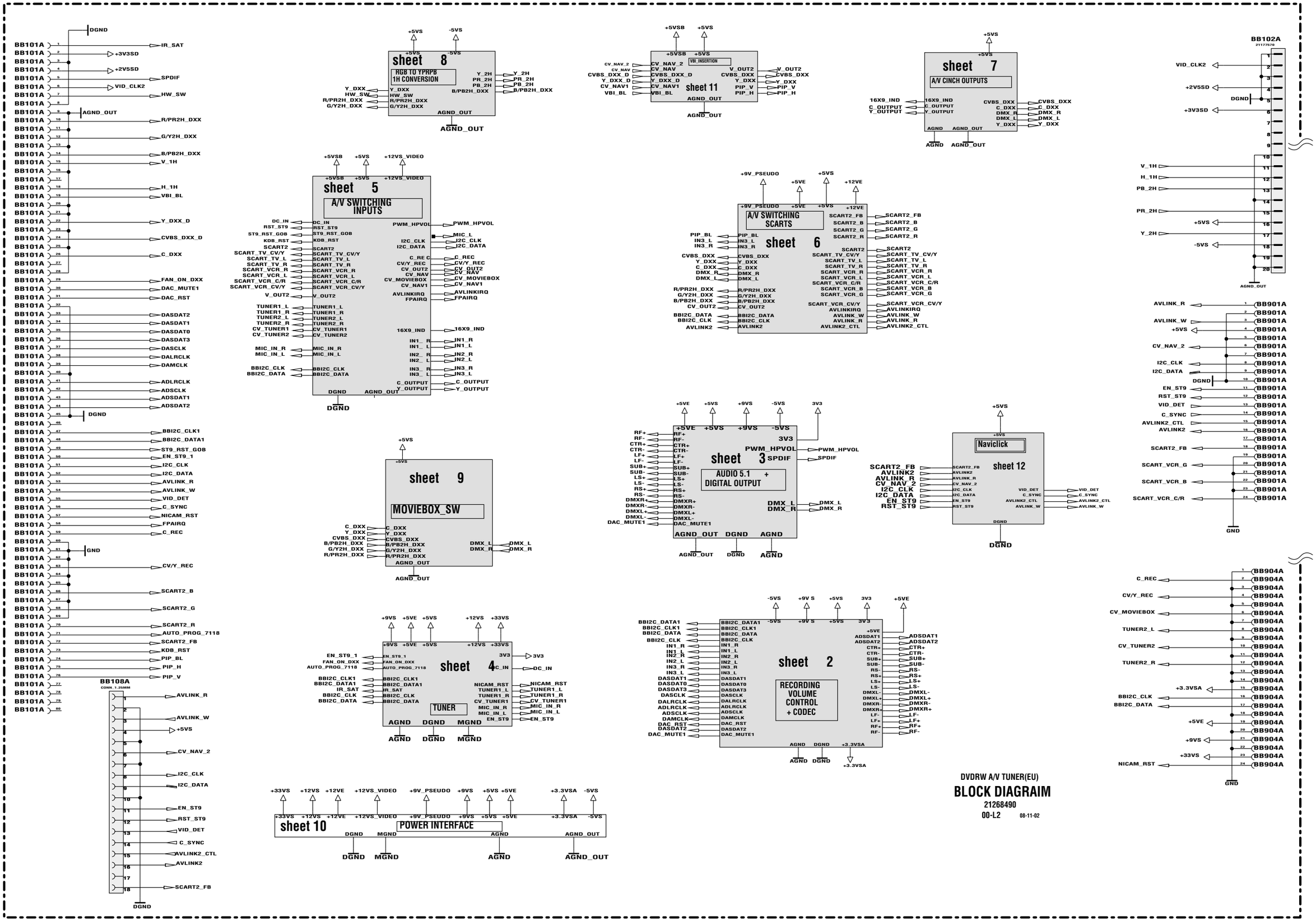
SCHEMATIC DIAGRAM - SCHEMA DE PRINCIPE - SCHALTBILD - SCHEMA - ESQUEMA


Note :
Power Supply primary circuit measurements.
- Use only (PGND) connection point.
Attention :
Mesure dans la partie primaire de l'alimentation
- Utiliser la masse du bloc alimentation (PGND).
Achtung :
Bei Messungen im Primärnetzteil
- Primärnetzteilmasse verwenden (PGND).
Attenzione :
misure nell'alimentatore primario
- usare massa alimentazione primario (PGND).
Cuidado :
Medida en el bloque de alimentacion
- Utilizar la masa del bloque de alimentacion (PGND).
Safety Part
When repairing, use original part only
Pièce de securité
N'utilisez que les pièces d'origine
Sicherheitsbauteil
Bei Ersatz nur Originalteil verwenden
Componenti di sicurezza
Per la riparazione utilizzare solo componenti originali
Utilice solo piezas originales
Pieza de seguridad
These components are not inserted for BOX3 and BOX4
FOR #1 REFER TO TABLE BELOW
BOX2 BOX3
DP065 RGP15G (PREP)
DP066 (PREP) S410D
Part of board connected to mains supply.
Partie du châssis reliée au secteur.
Primärseite des Netzteils.
Parte dello châssis collegata alla rete.
Parte del chassis conectar a la red.
Use isolating mains transformer.
Utiliser un transformateur isolateur du secteur.
Einen Trenntrafo verwenden.
Utilizar un transformador aislador de red.
Utilizzare un trasformatore per isolarvi dalla rete.
CIRCUIT_DIAGRAM_SMPS_EU_IDR02
2118137E
BB201P
BB301P
BB401P
BB601P
BB701P
BD100P

DTH7500 / DTH8000
First issue 12 / 03

SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

(AV TUNER BOARD 1/11)



DVDRW A/V TUNER(EU)

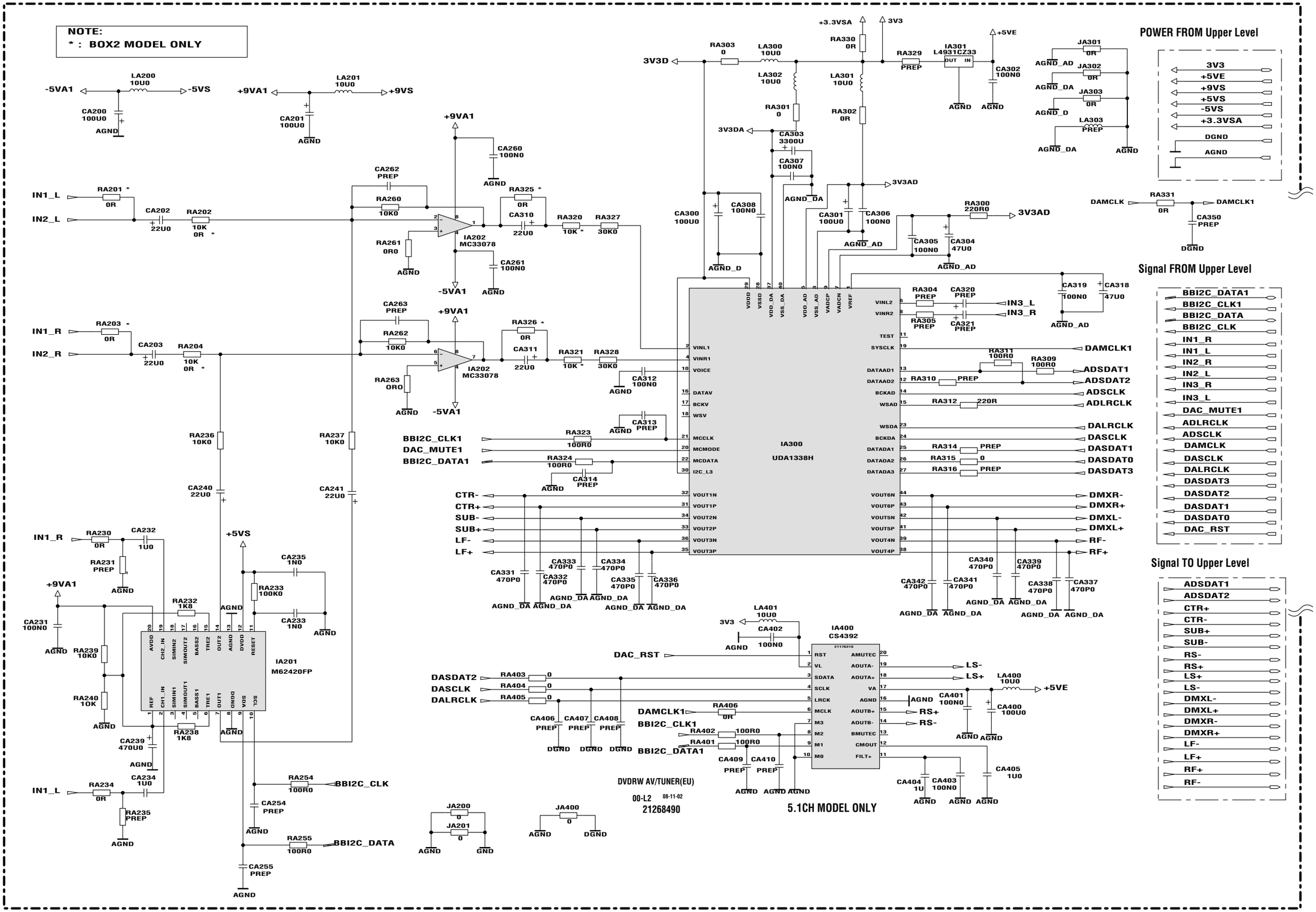
BLOCK DIAGRAIM

21268490

00-L2 08-11-02

SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

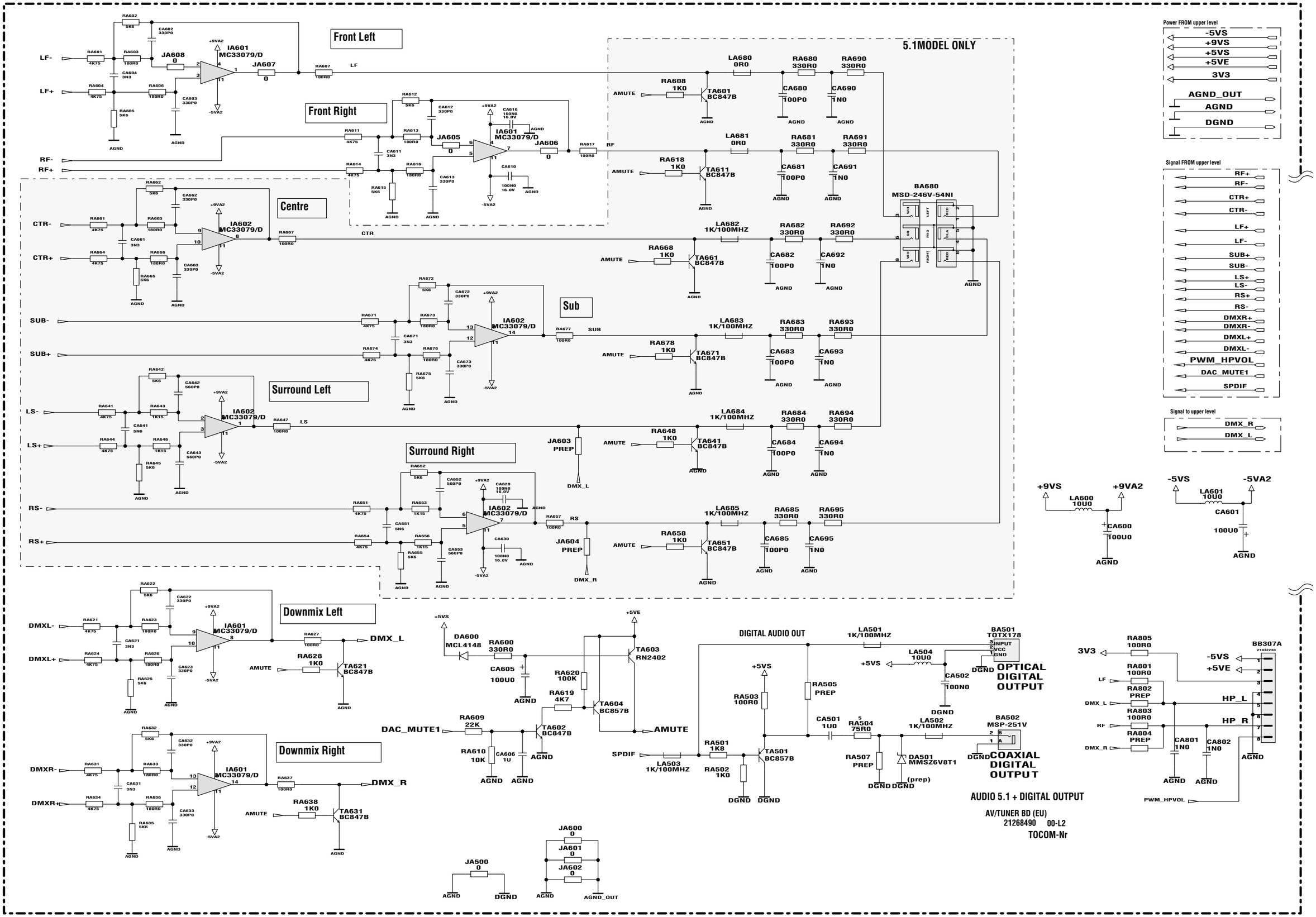
(AV TUNER BOARD 2/11)



DTH7500 / DTH8000
First issue 12 / 03

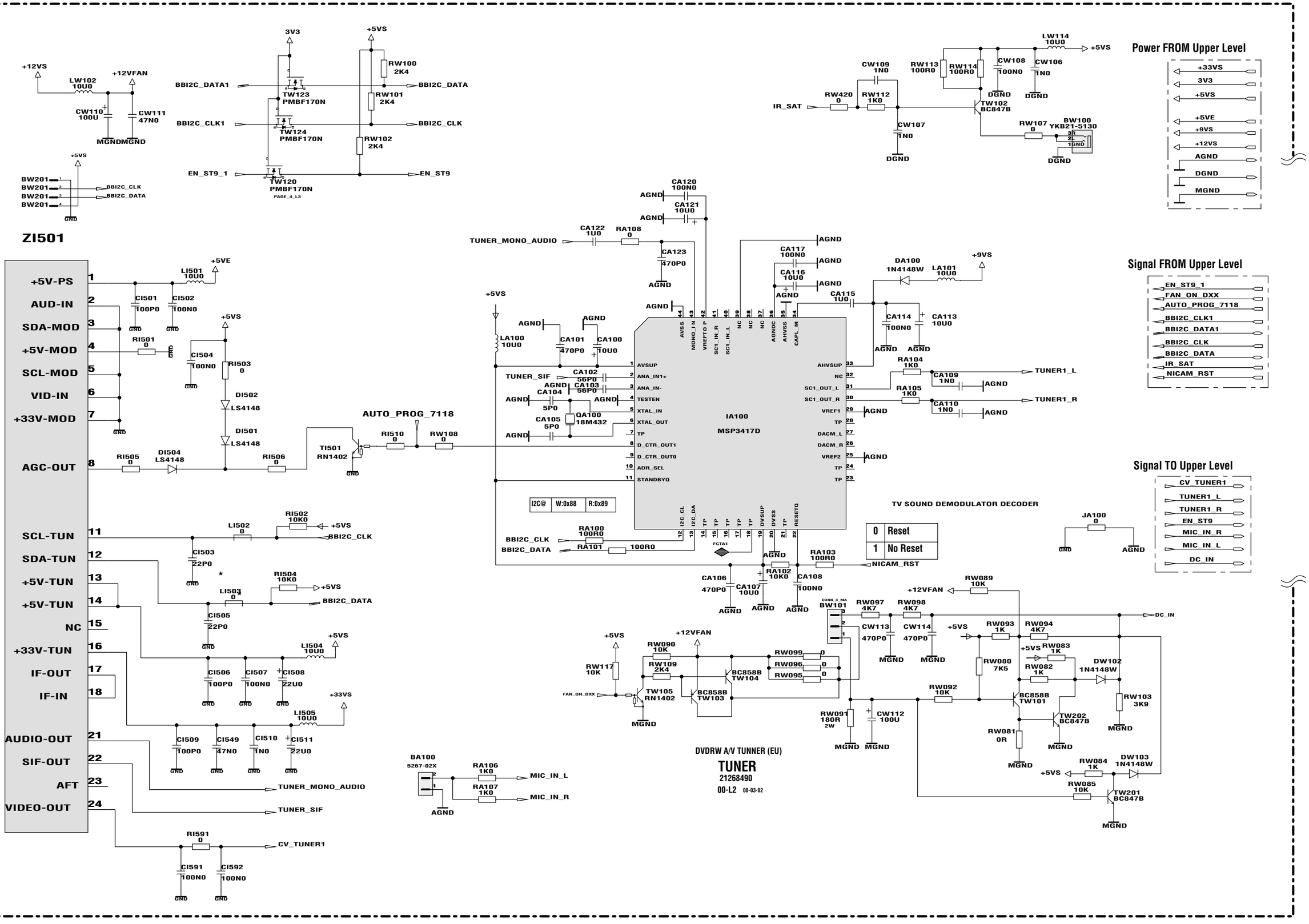
SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

(AV TUNER BOARD 3/11)



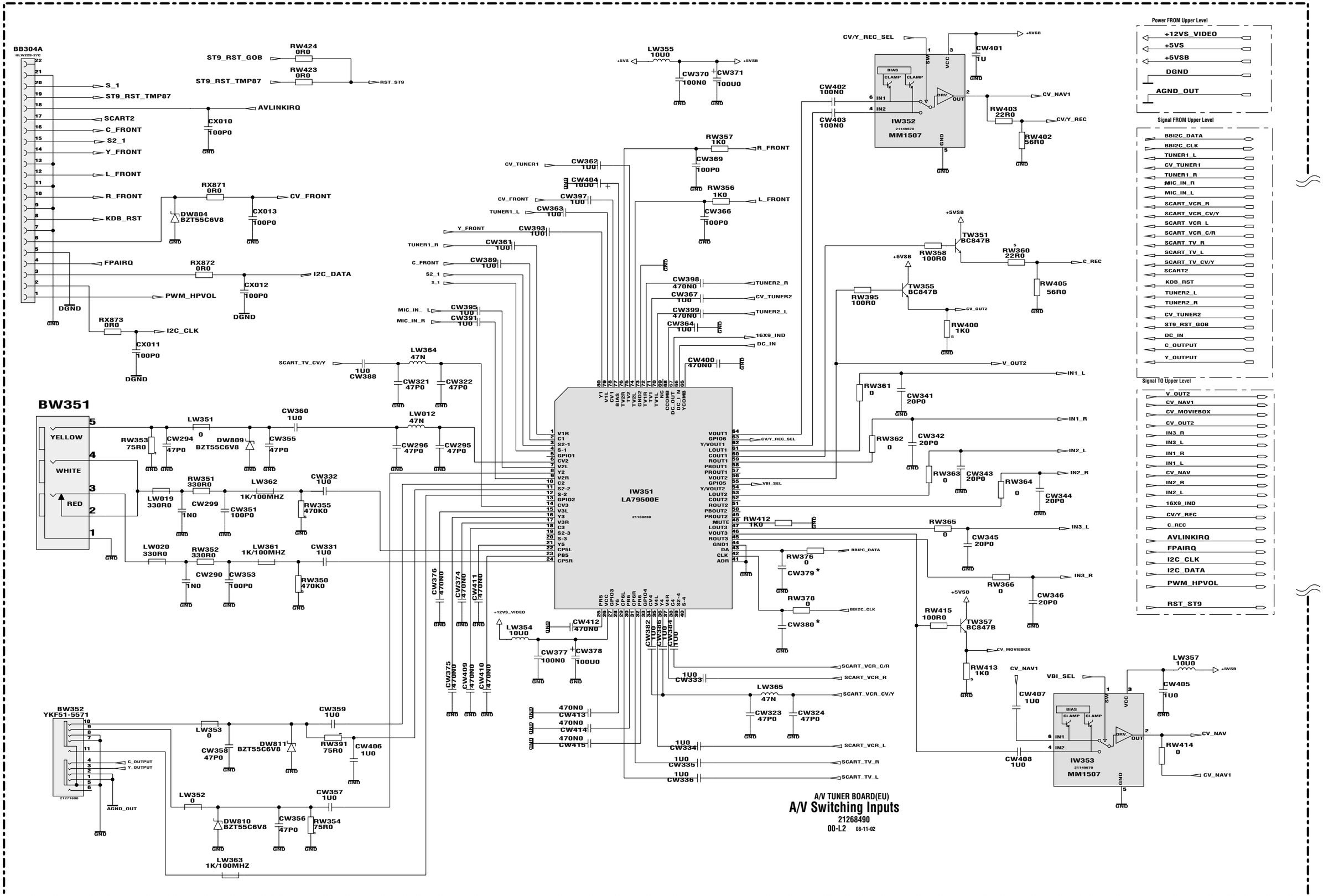
SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

(AV TUNER BOARD 4/11)



SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

(AV TUNER BOARD 5/11)



A/V TUNER BOARD(EU)

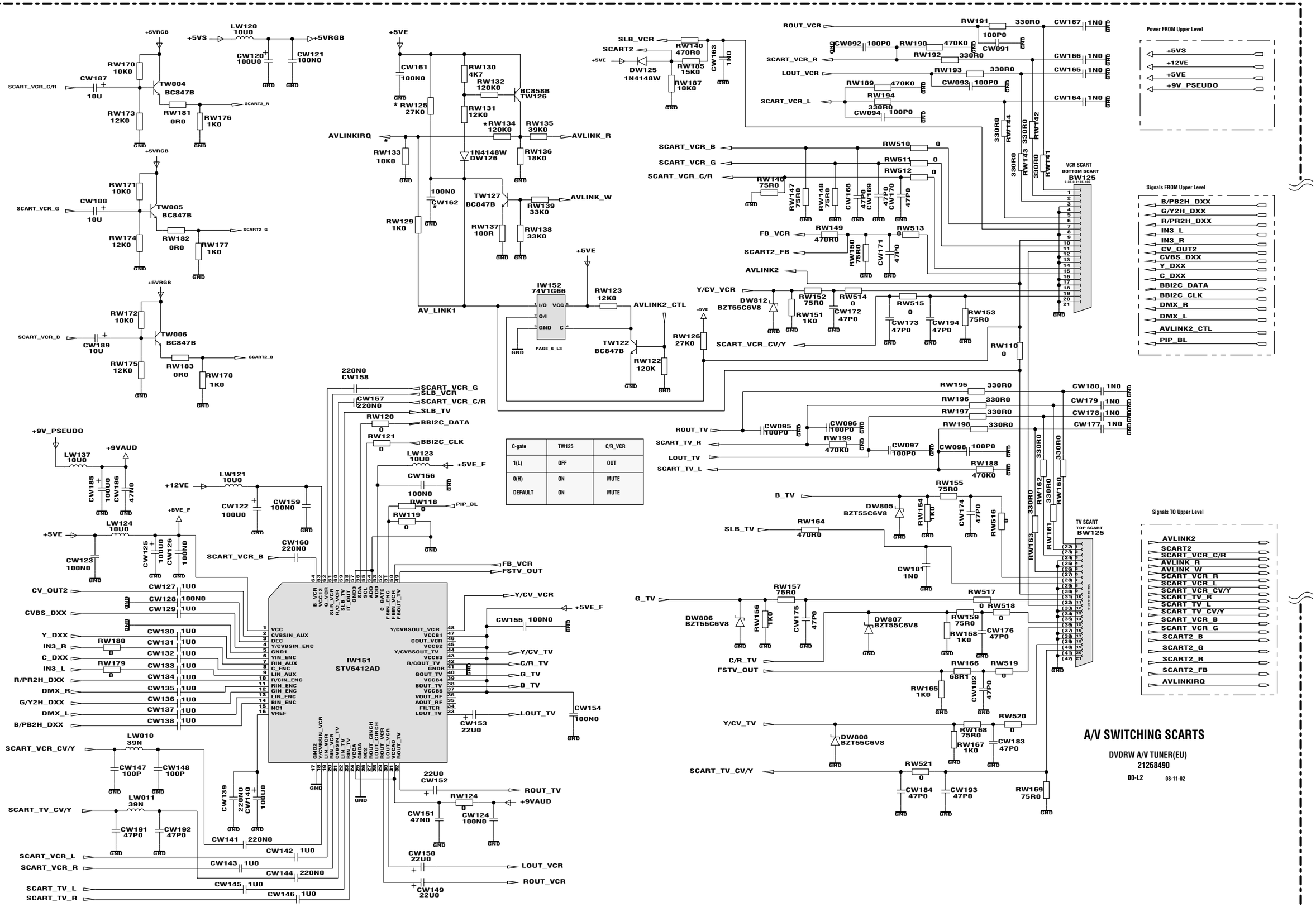
A/V Switching Inputs

21268490

00-L2 08-11-02

SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

(AV TUNER BOARD 6/11)

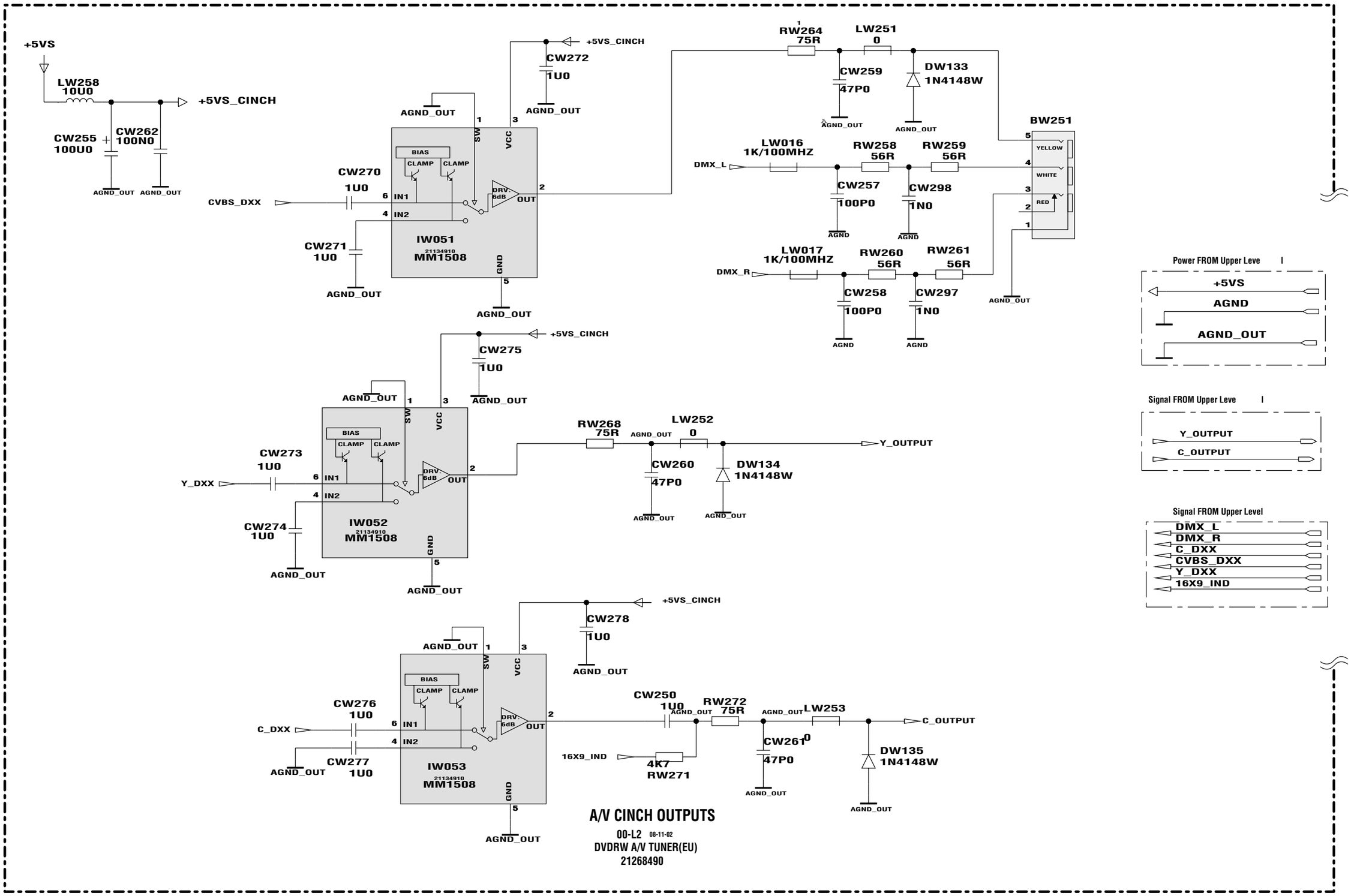


C-gate	TW125	C/R_VCR
1(L)	OFF	OUT
0(H)	ON	MUTE
DEFAULT	ON	MUTE

DTH7500 / DTH8000
First issue 12 / 03

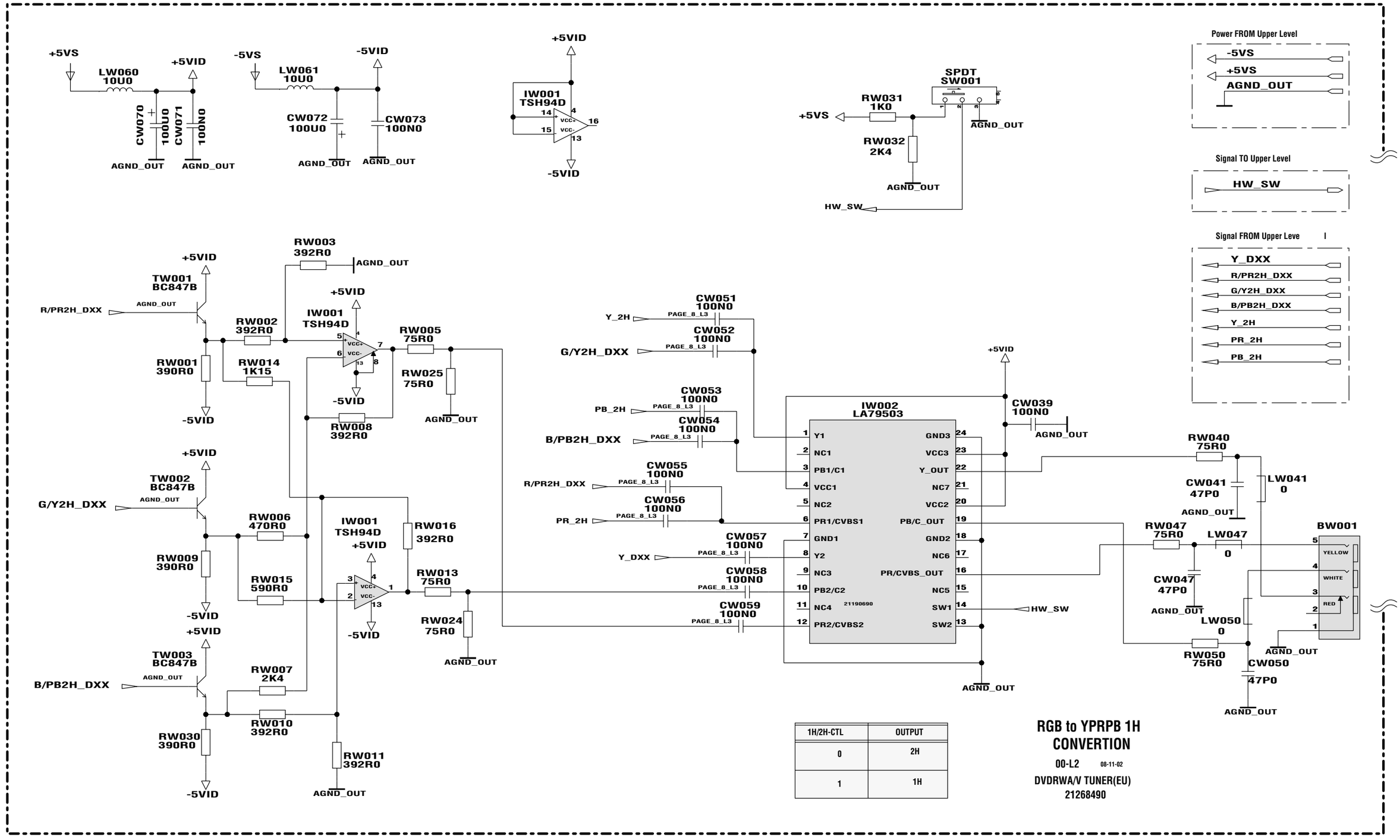
SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

(AV TUNER BOARD 7/11)



SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

(AV TUNER BOARD 8/11)



1H/2H-CTL	OUTPUT
0	2H
1	1H

RGB to YPRPB 1H CONVERTION

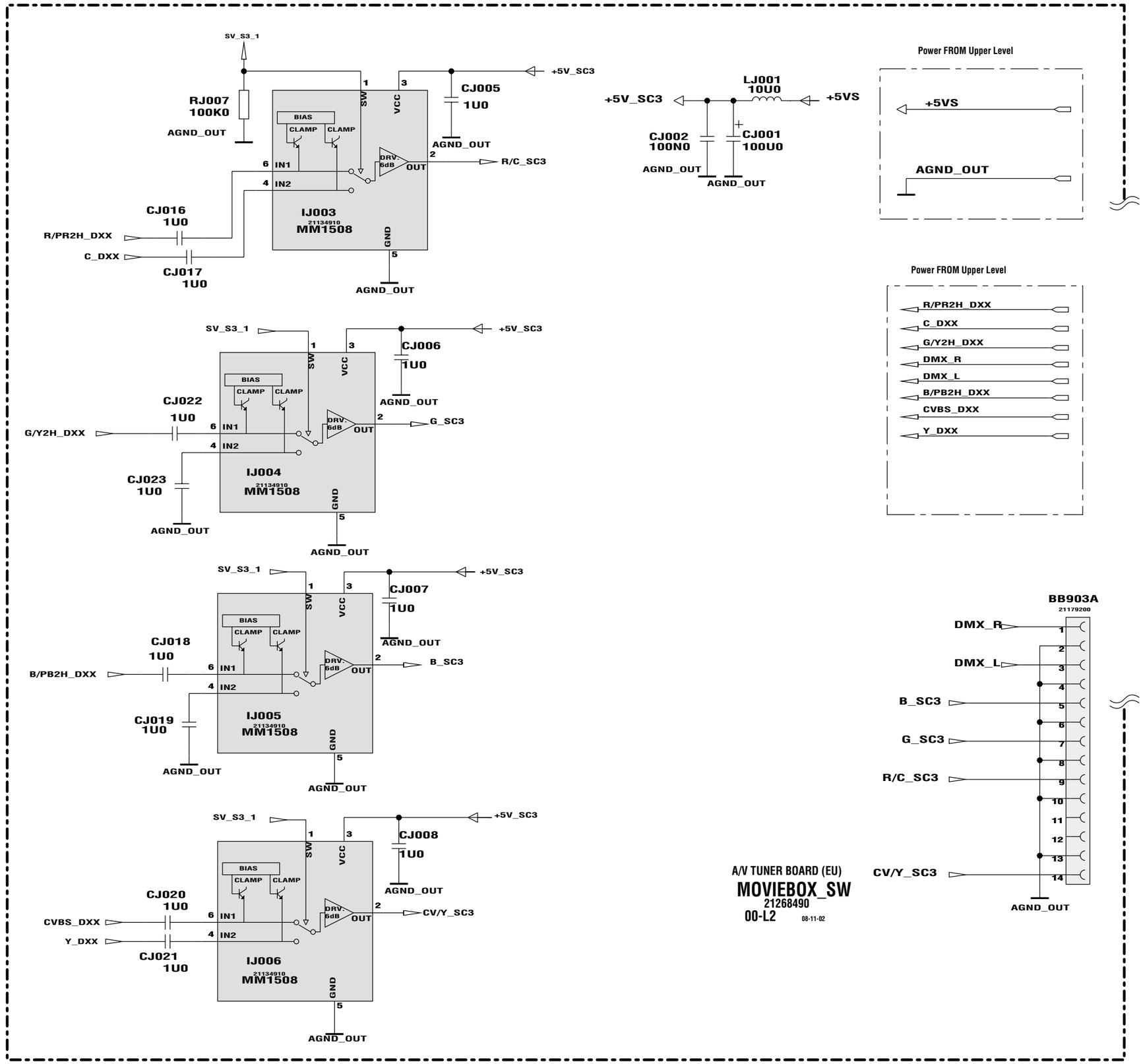
00-L2 08-11-02

DVDRWA/V TUNER(EU)

21268490

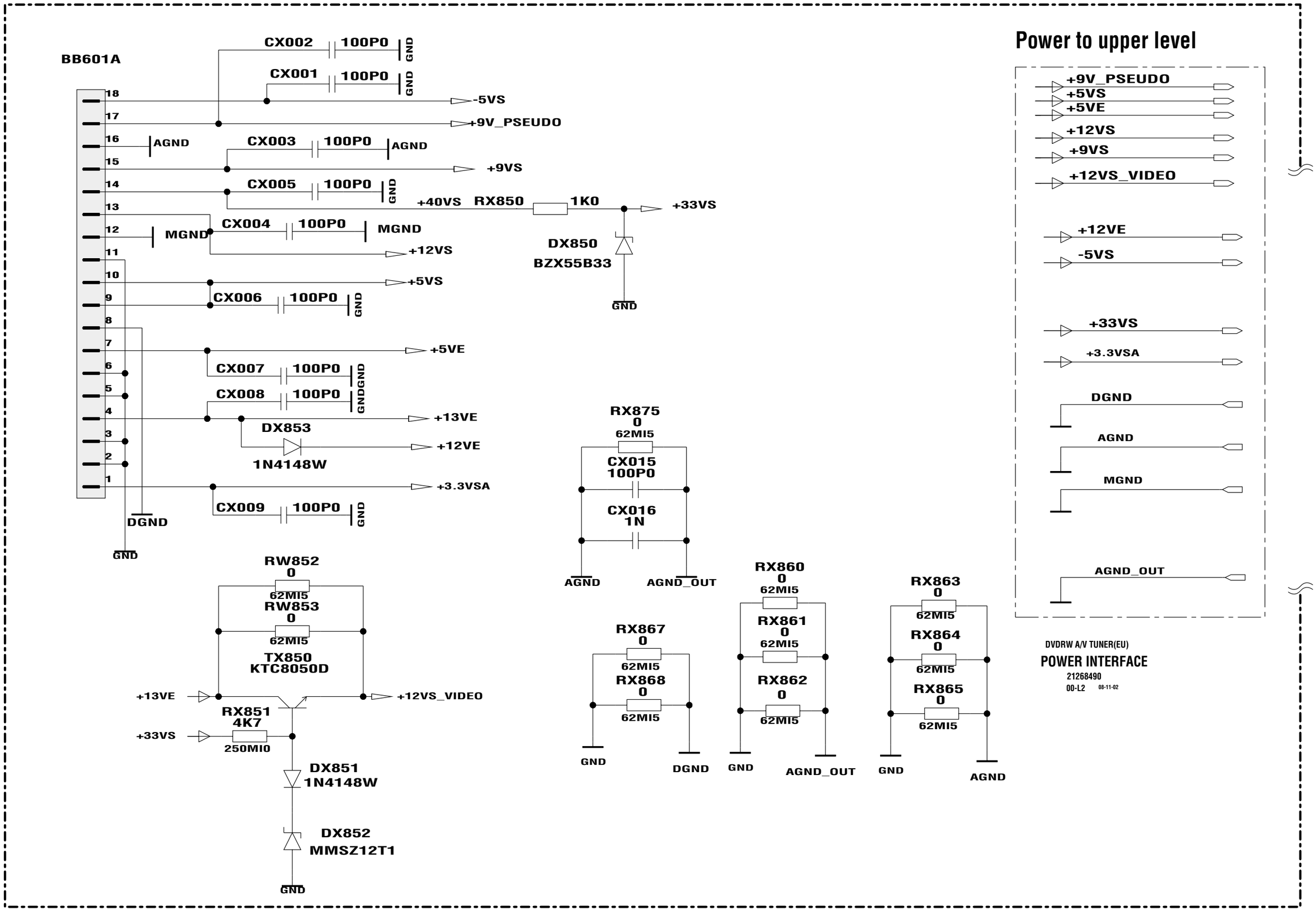
SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

(AV TUNER BOARD 9/11)



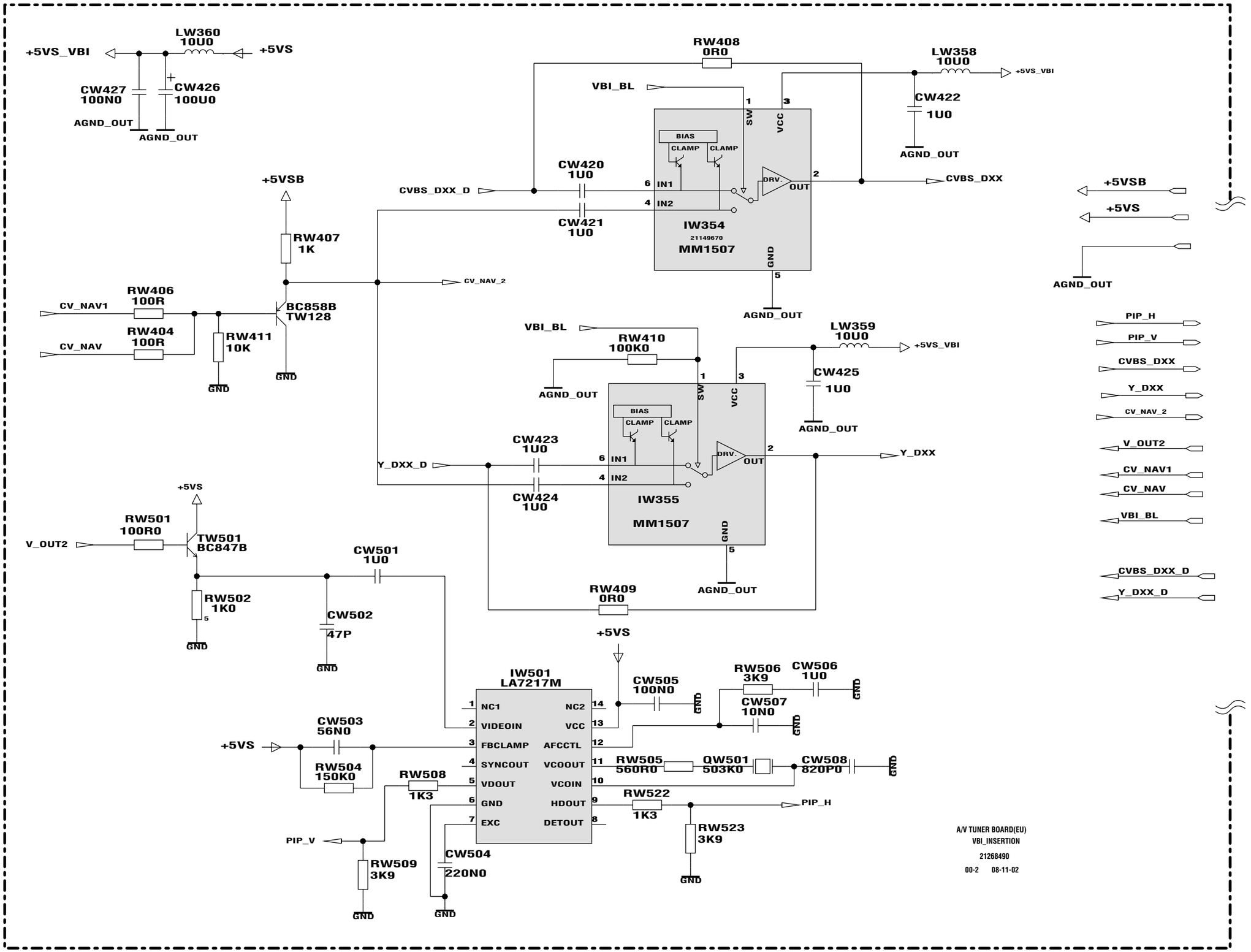
SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER
SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

(AV TUNER BOARD 10/11)



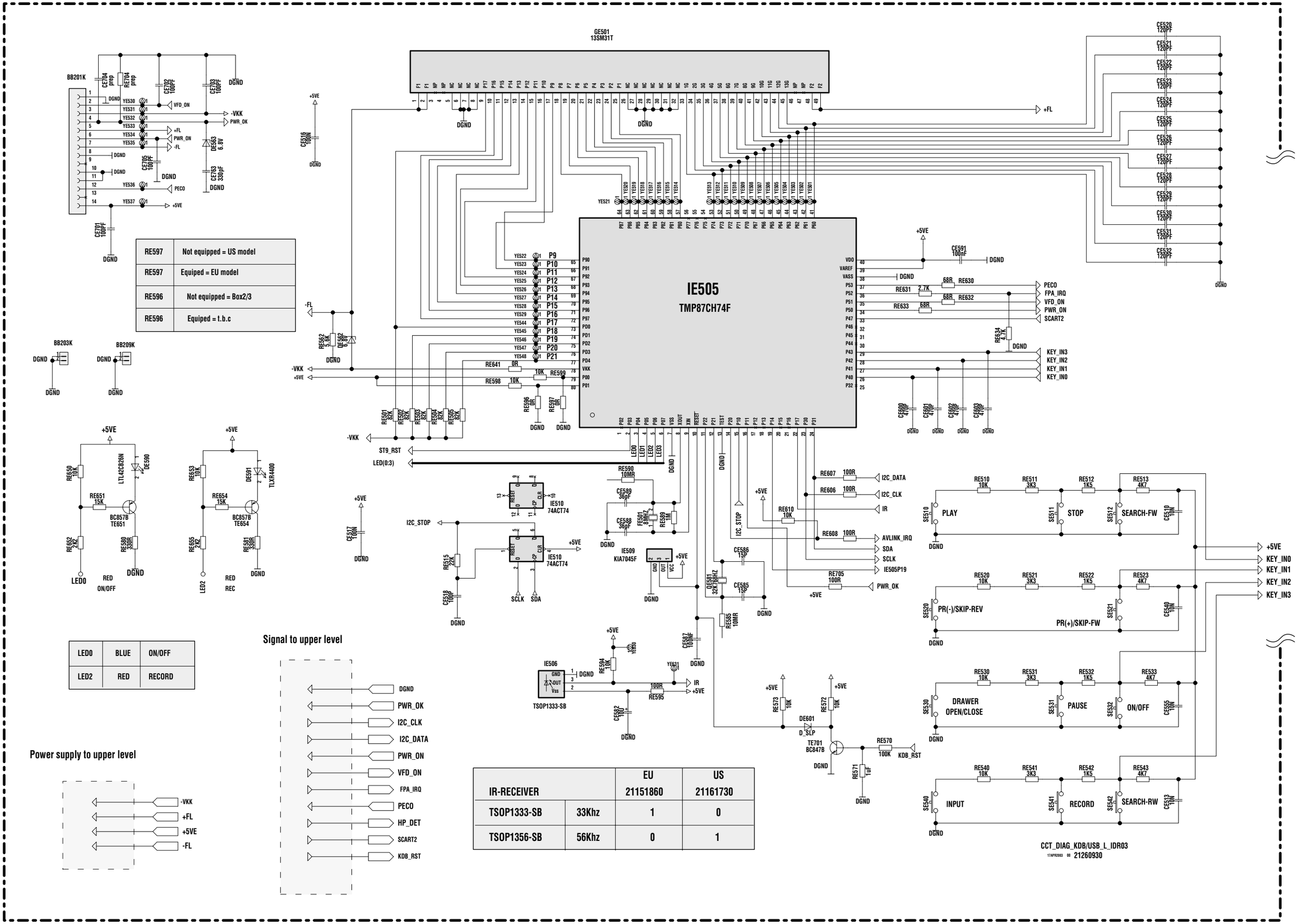
SCART INTERFACE / TUNER SCHEMATIC DIAGRAM - SCHEMA DE L'INTERFACE PERITELEVISION / TUNER - SCHALTBILD EUROPA NORMBUCHSE / TUNER SCHEMA DELLA PRESA PERITEL / SINTONIZZATORE - ESQUEMA INTERFAZ EUROTOMA / SINTONIZADOR

(AV TUNER BOARD 11/11)



KEYBOARD WITH DISPLAY - PLATINE DE COMMANDES AVEC AFFICHEUR - BEDIENTEIL MIT DISPLAY - TASTIERA CON DISPLAY - PLATINA MANDOS CON VISUALIZADOR

SCHEMATIC DIAGRAM - SCHEMA DE PRINCIPE - SCHALTBILD - SCHEMA - ESQUEMA

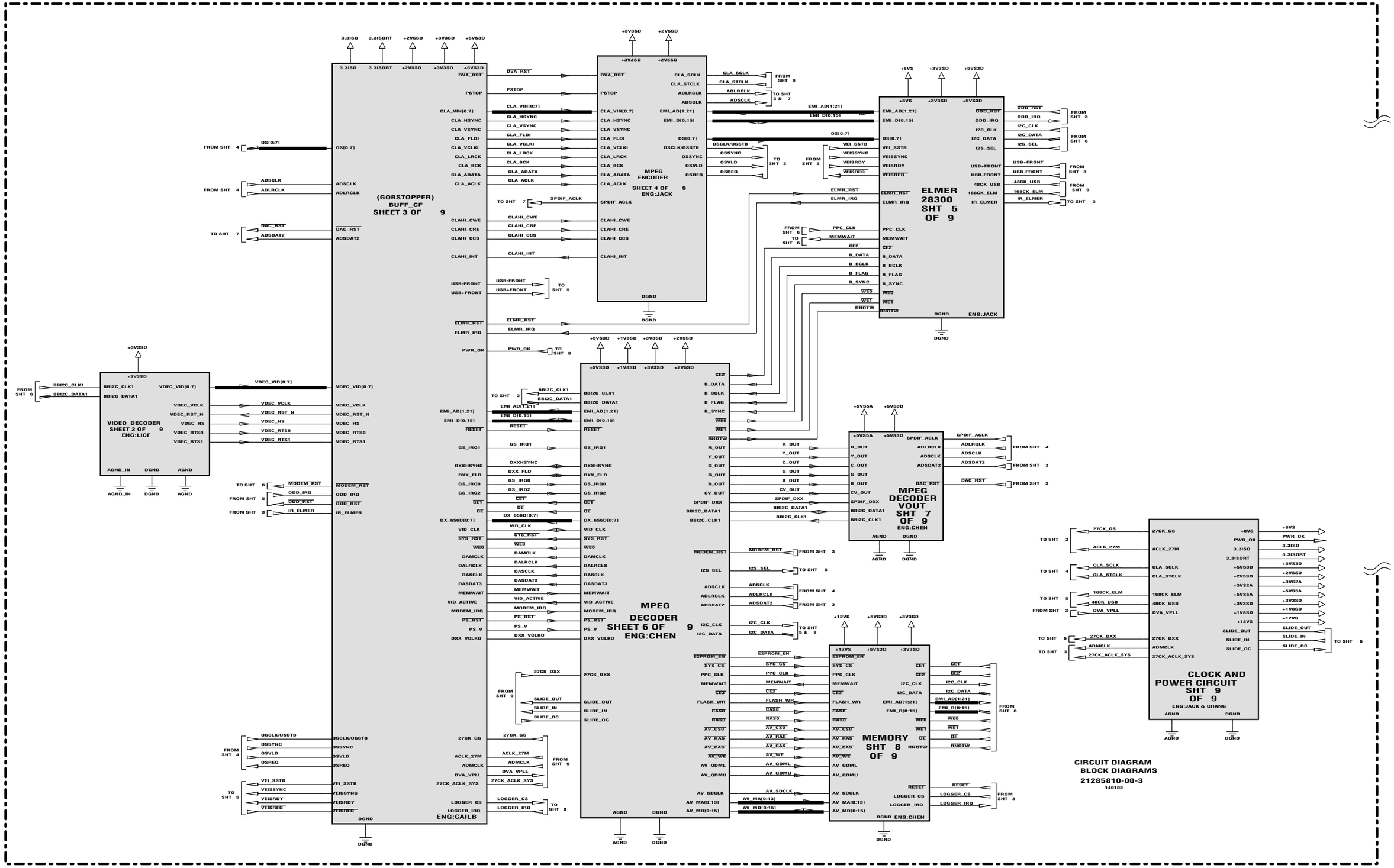


DTH7500 / DTH8000

First issue 12 / 03

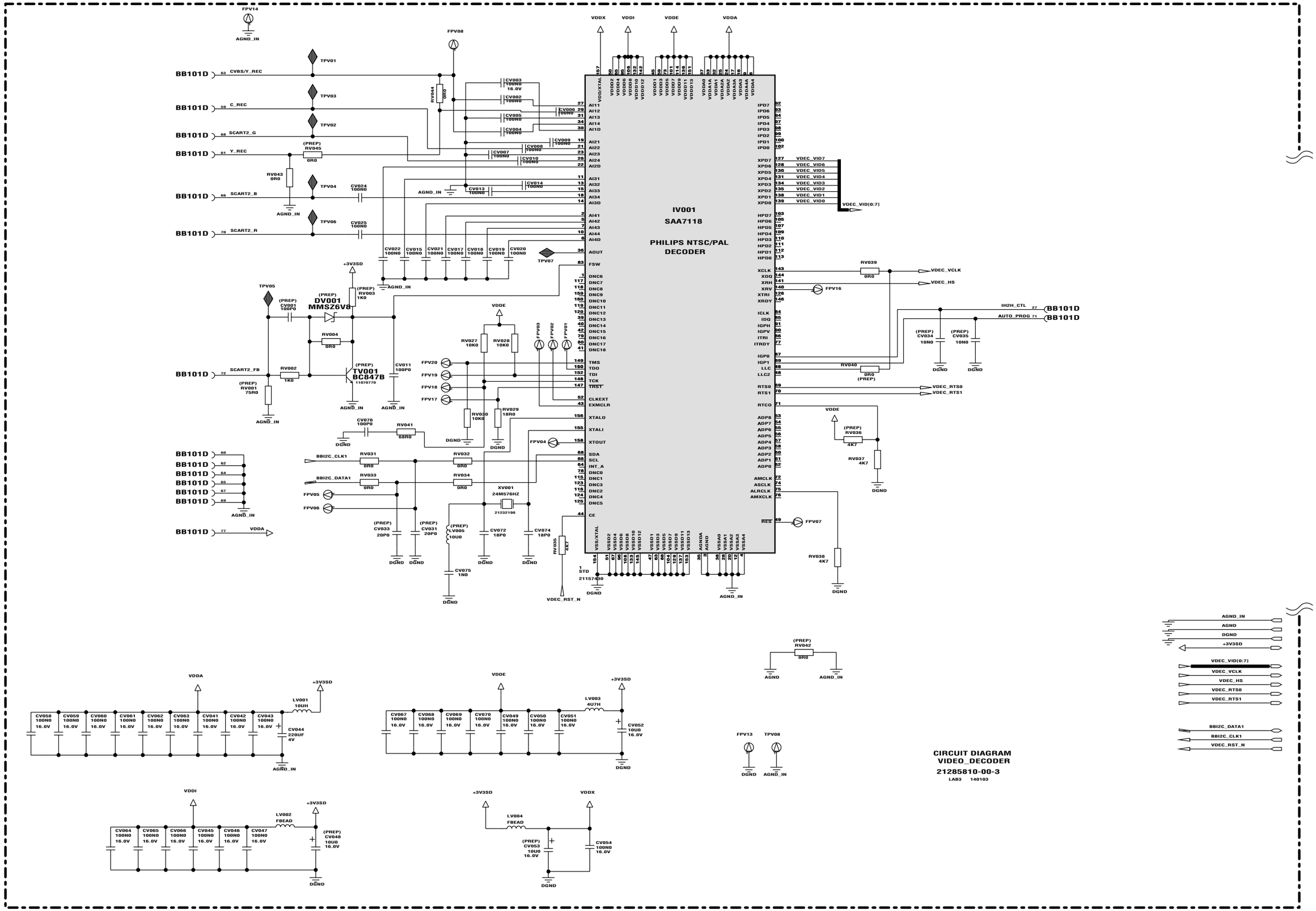
MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 1/9)


(GOBSTOPPER) BUFF_CF SHEET 3 OF 9
MPEG ENCODER SHEET 4 OF 9 ENG:JACK
ELMER 28300 SHT 5 OF 9 ENG:JACK
VIDEO_DECODER SHEET 2 OF 9 ENG:LICF
MPEG DECODER SHEET 6 OF 9 ENG:CHEN
MPEG DECODER VOUT SHT 7 OF 9 ENG:CHEN
MEMORY SHT 8 OF 9 ENG:CHEN
CLOCK AND POWER CIRCUIT SHT 9 OF 9 ENG:JACK & CHANG
CIRCUIT DIAGRAM BLOCK DIAGRAMS 21285810-00-3 140103

MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

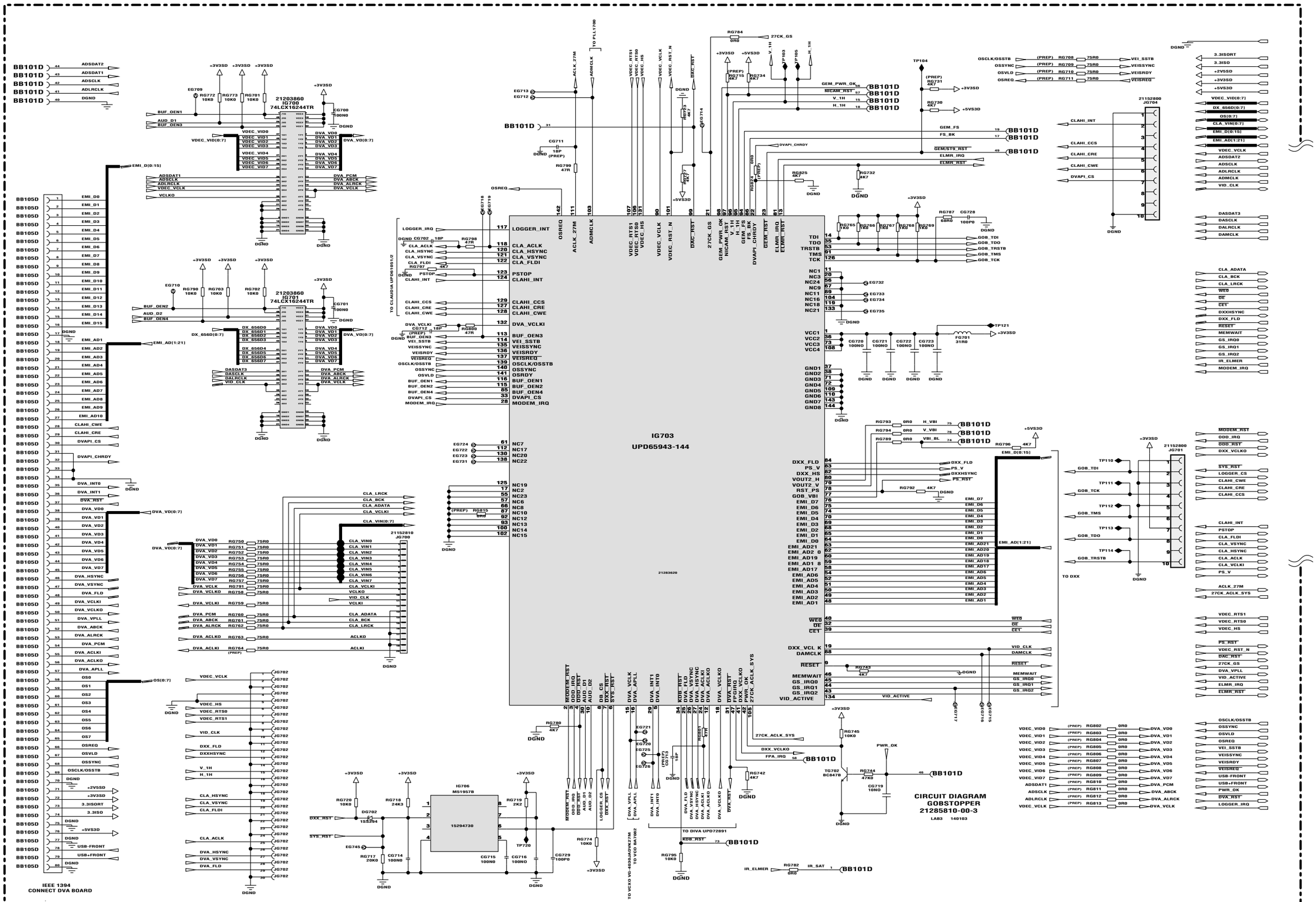
(DIGITAL BOARD 2/9)



DTH7500 / DTH8000
First issue 12 / 03

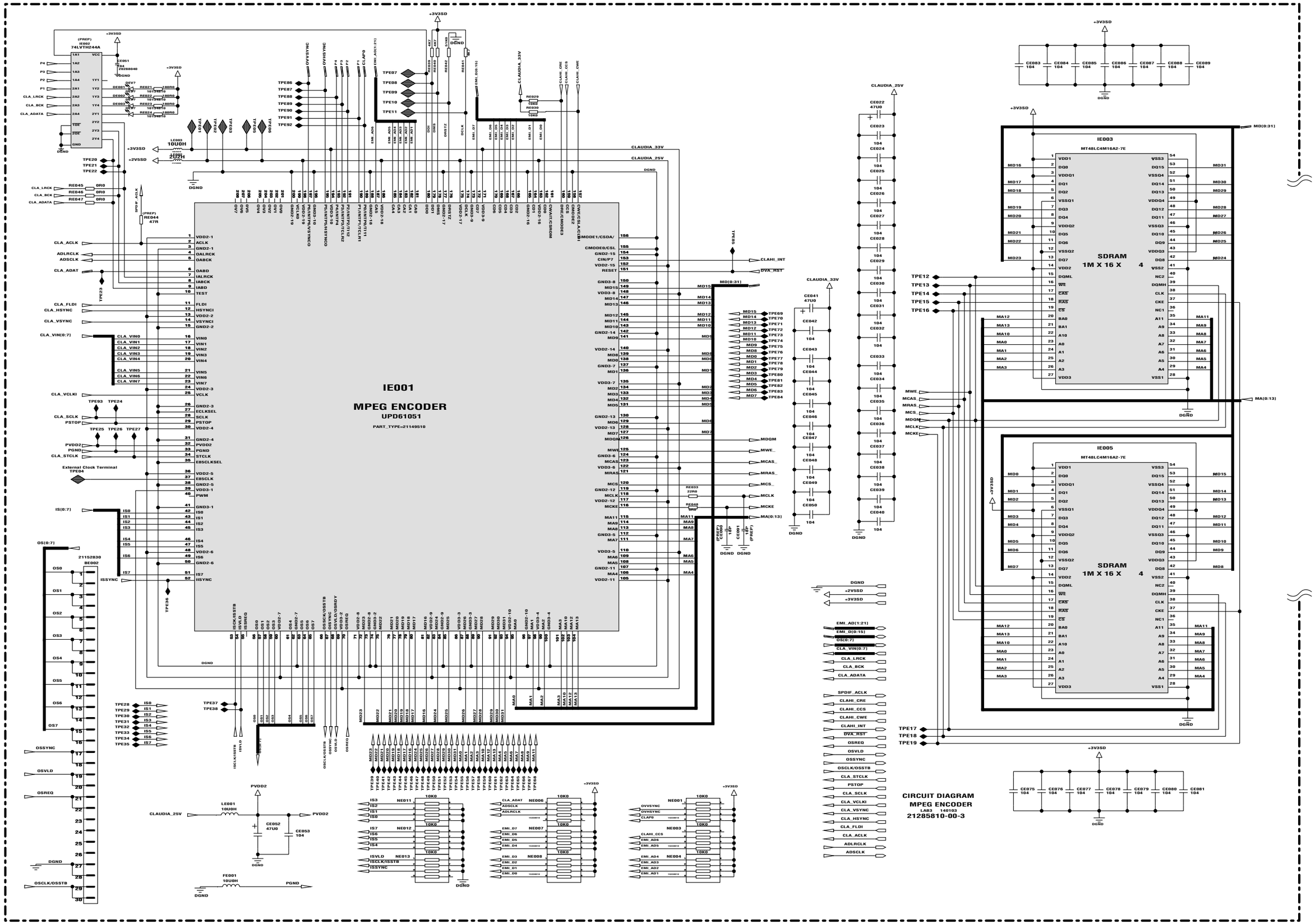
MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 3/9)



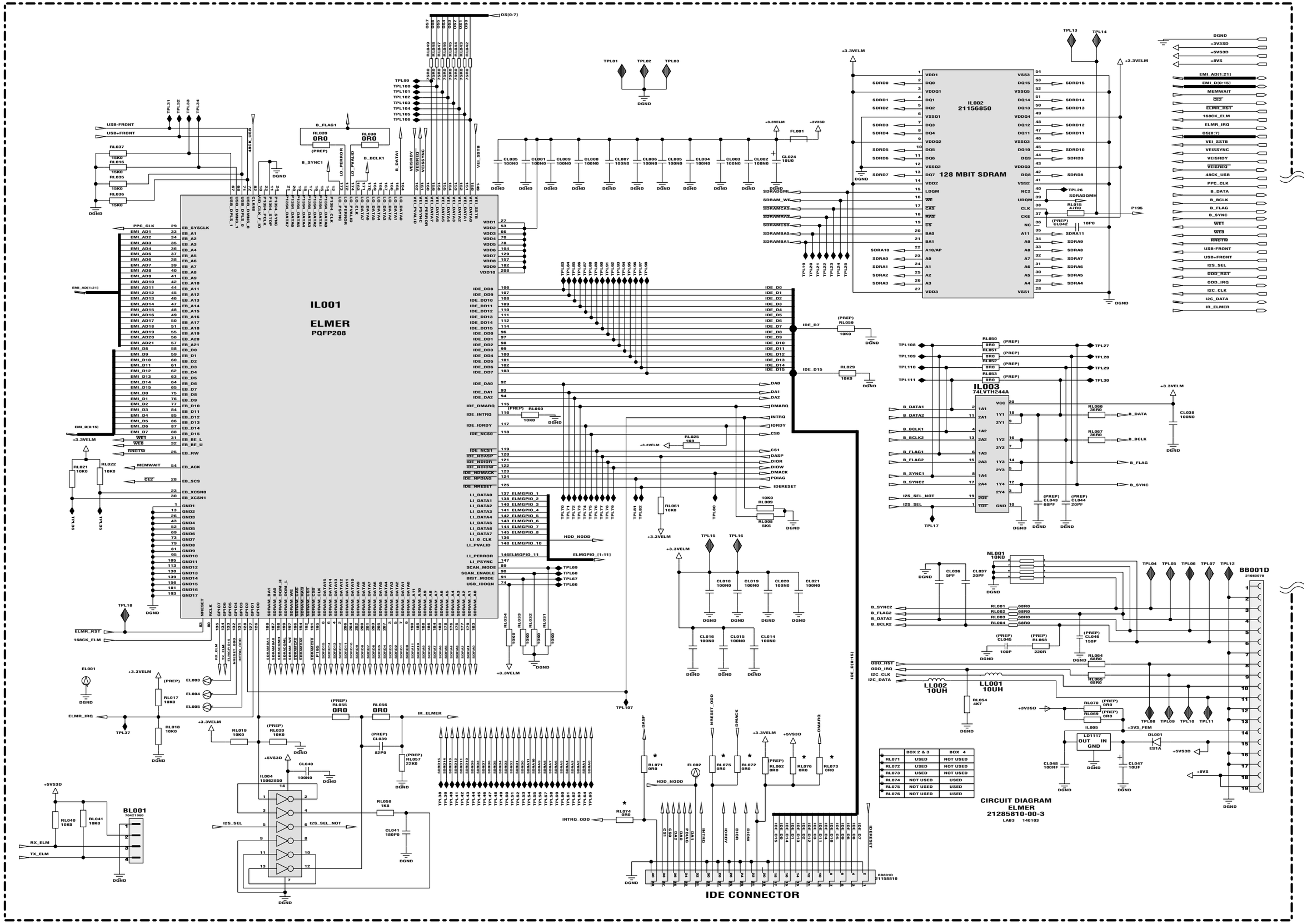
MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 4/9)



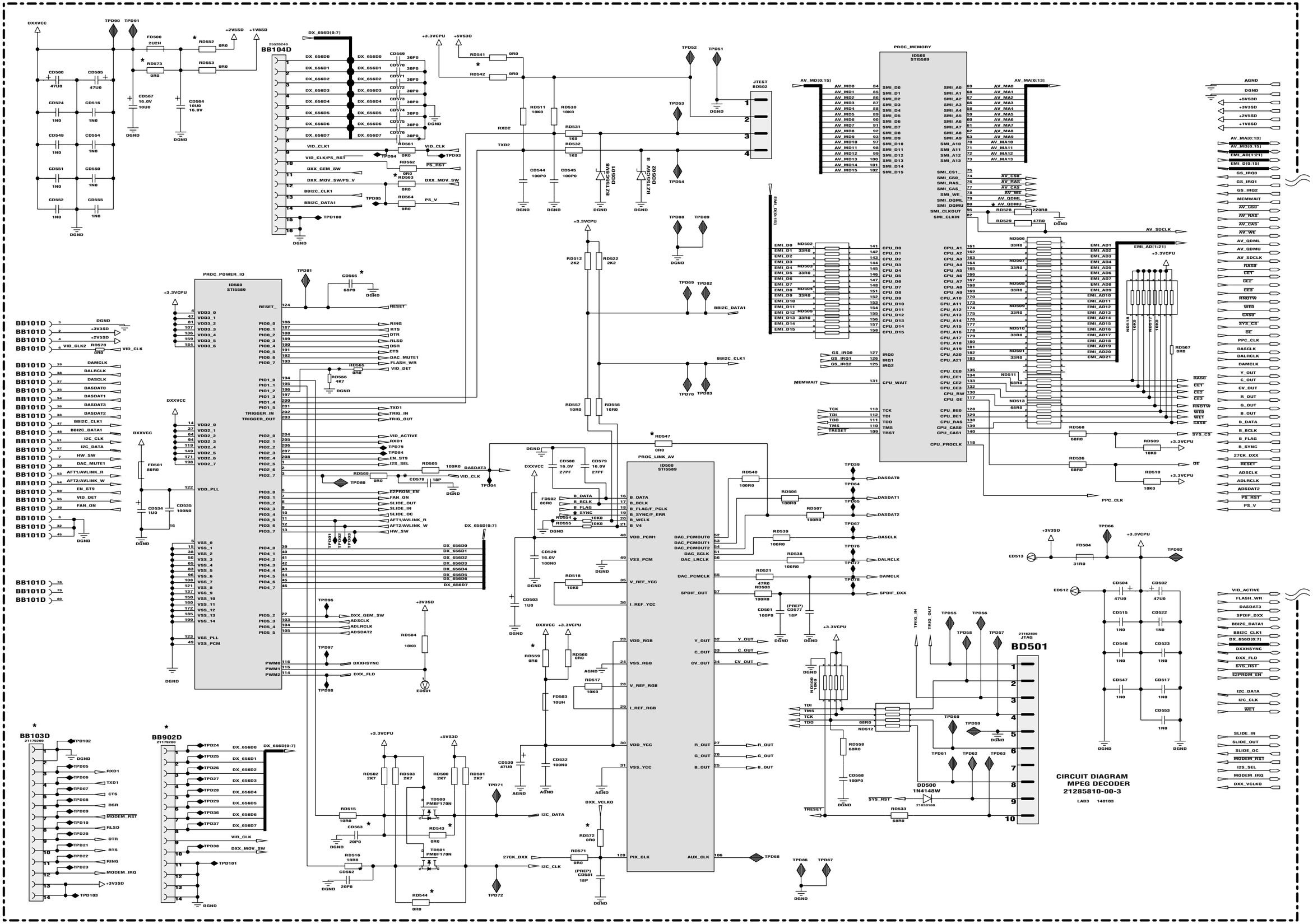
MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 5/9)



MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

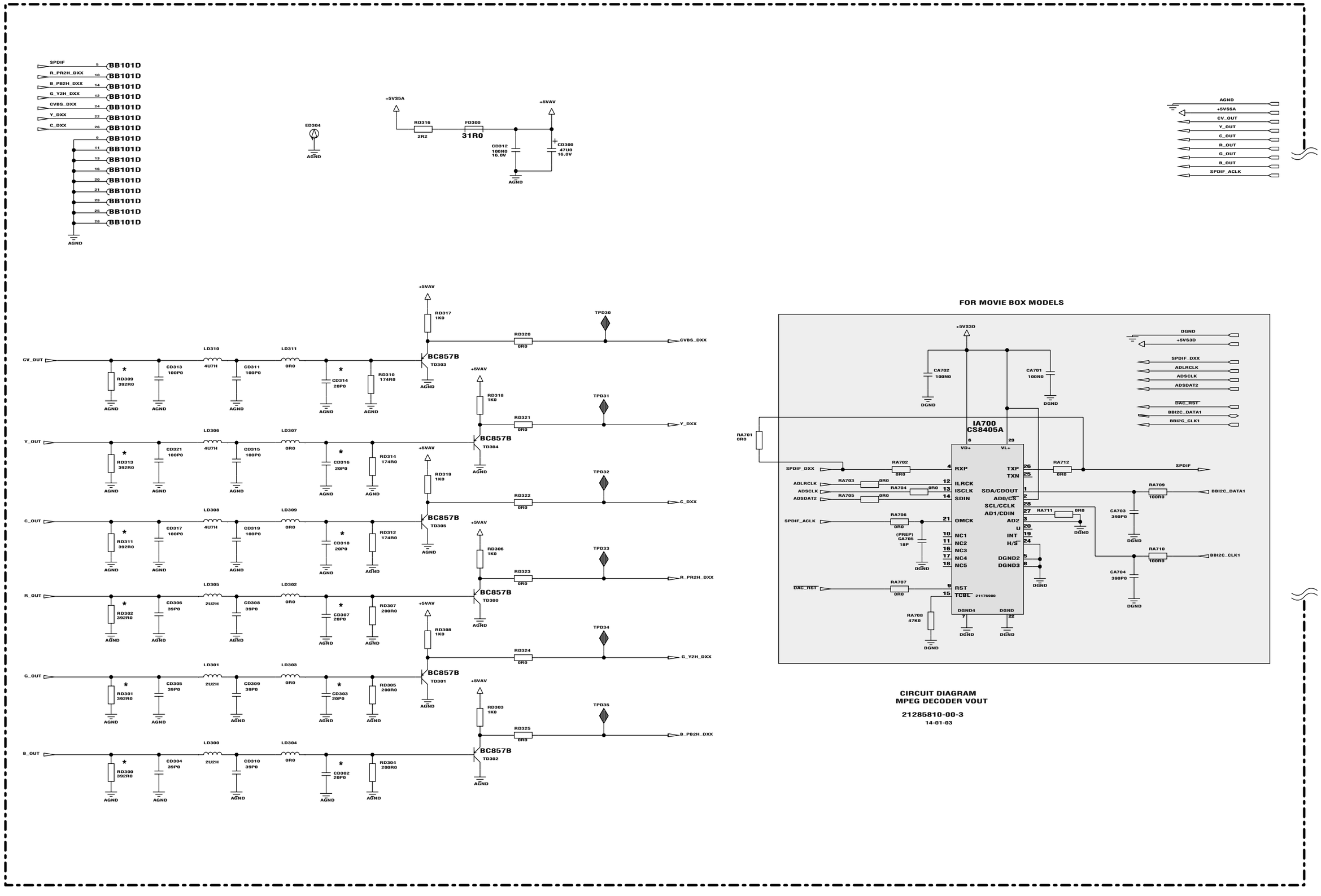
(DIGITAL BOARD 6/9)



DTH7500 / DTH8000
First issue 12 / 03

MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

(DIGITAL BOARD 7/9)

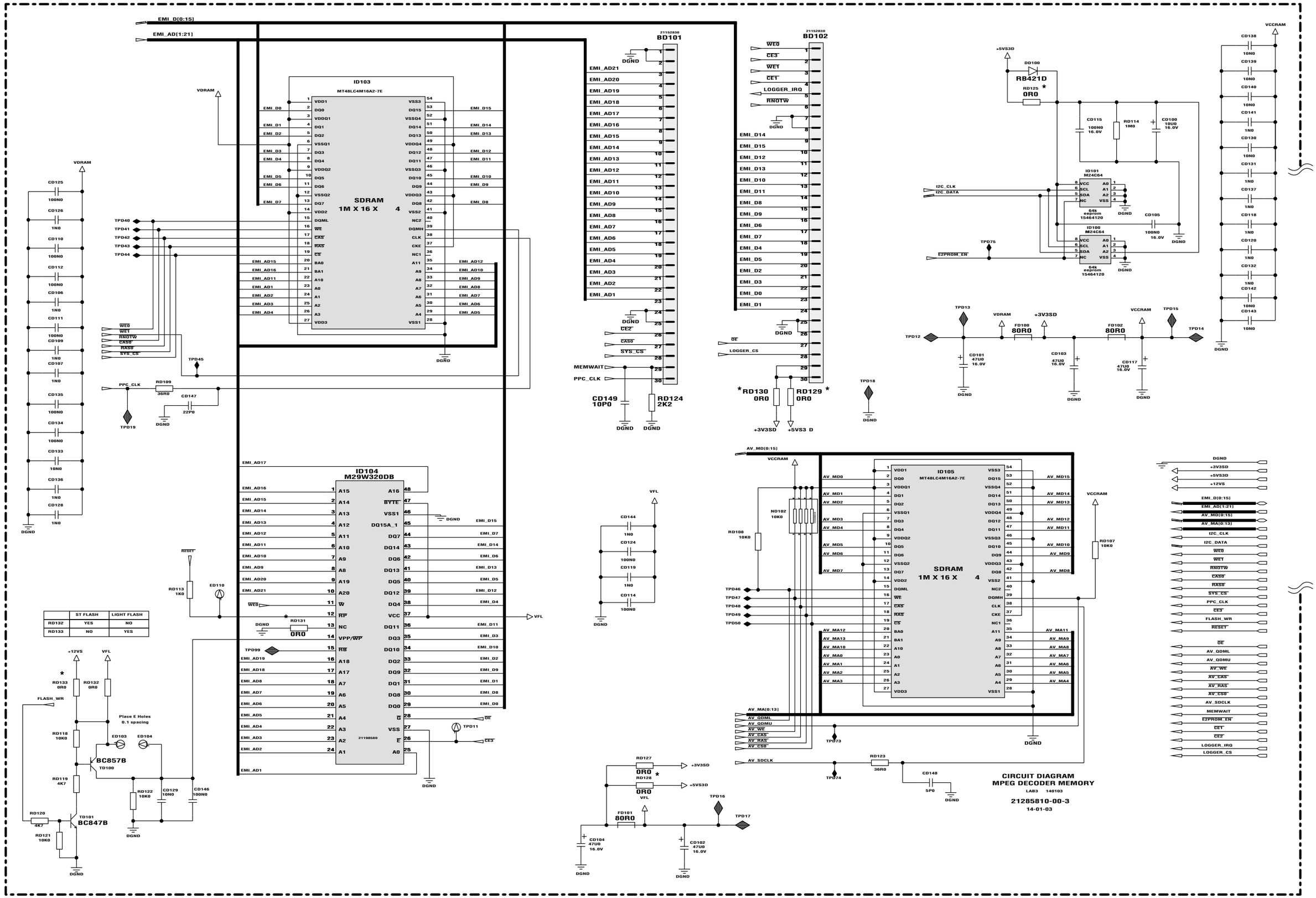


CIRCUIT DIAGRAM
MPEG DECODER VOUT

21285810-00-3
14-01-03

MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

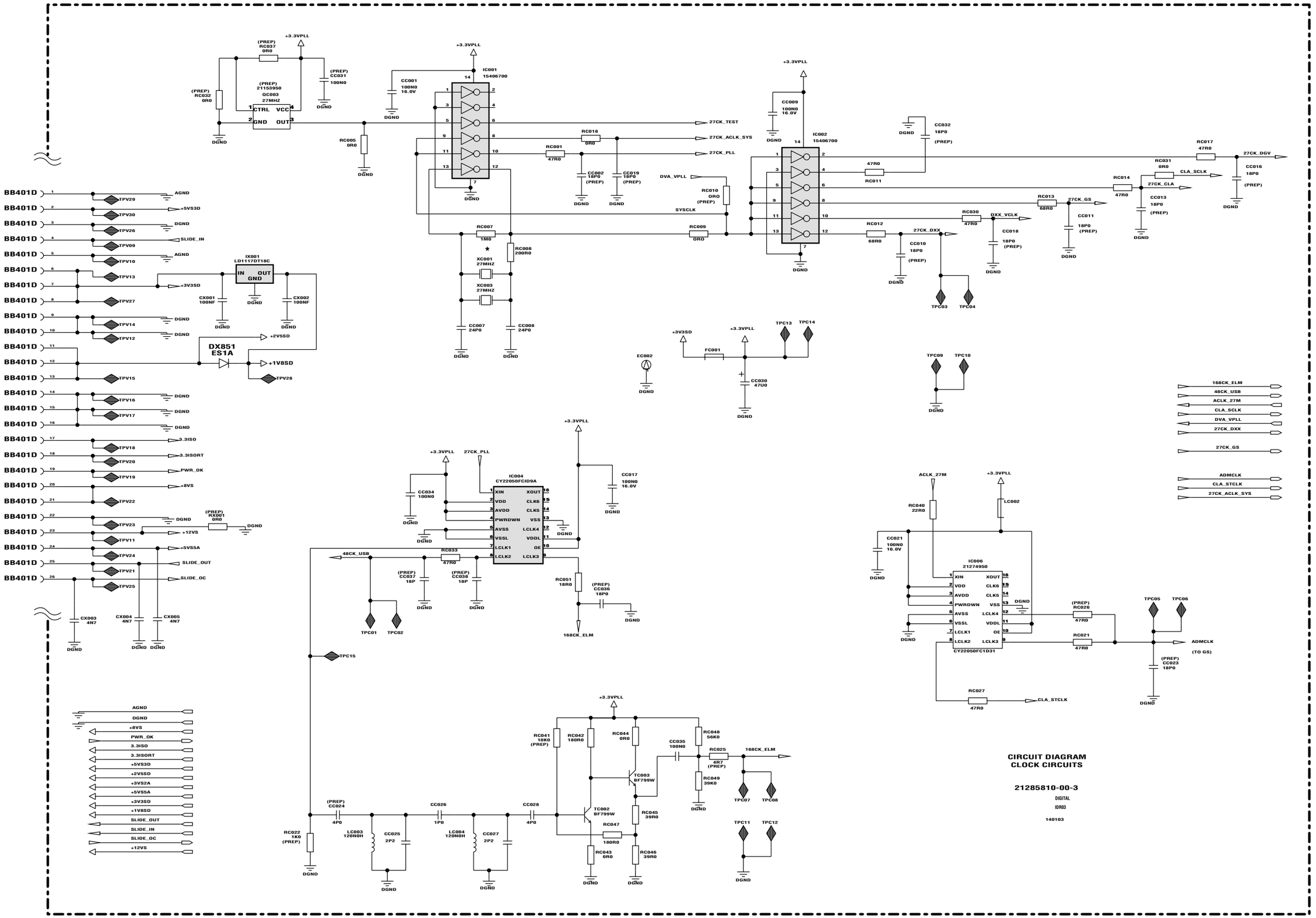
(DIGITAL BOARD 8/9)



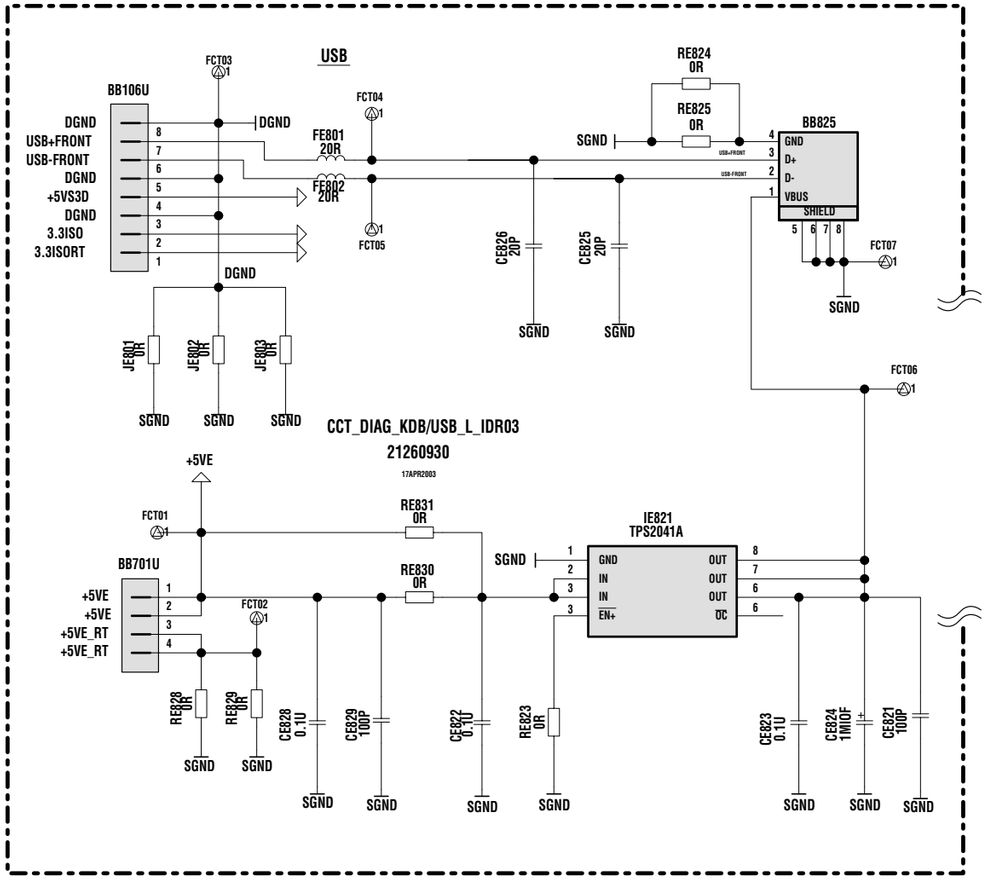
DTH7500 / DTH8000
First issue 12 / 03

MAIN SCHEMATIC DIAGRAM - SCHEMA DE LA PLATINE PRINCIPALE - SCHALTBILD HAUPTPLATINE - SCHEMA DELLA PIASTRA PRINCIPALE- ESQUEMA DE LA PLATINA PRINCIPAL

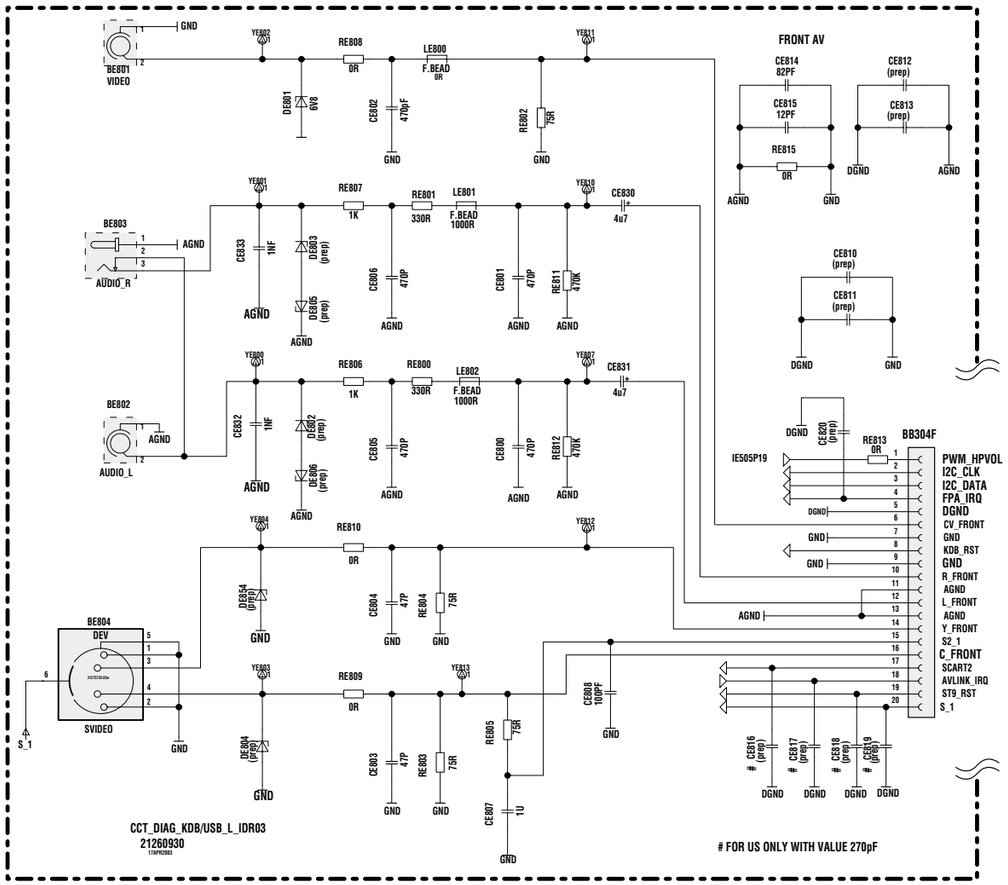
(DIGITAL BOARD 9/9)



USB MODULE - MODULE USB - USB MODUL - MODULO USB - MÒDULO USB

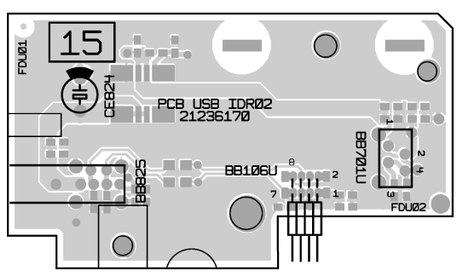


INPUT/OUTPUT - ENTREES/SORTIES - EINGÄNGE/AUSGÄNGE INGRESSO/USCITA - ENTRADA/SALIDA

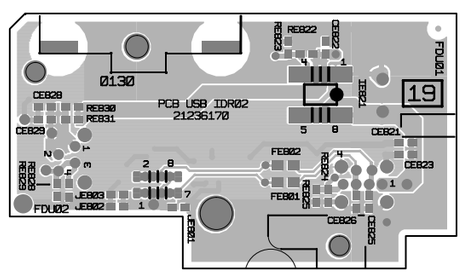


USB MODULE - MODULE USB - USB MODUL - MODULO USB - MÓDULO USB

COMPONENT SIDE - COTÉ COMPOSANTS - BESTÜCKUNGSSEITE - LATO COMPONENTI - LADO COMPONENTES

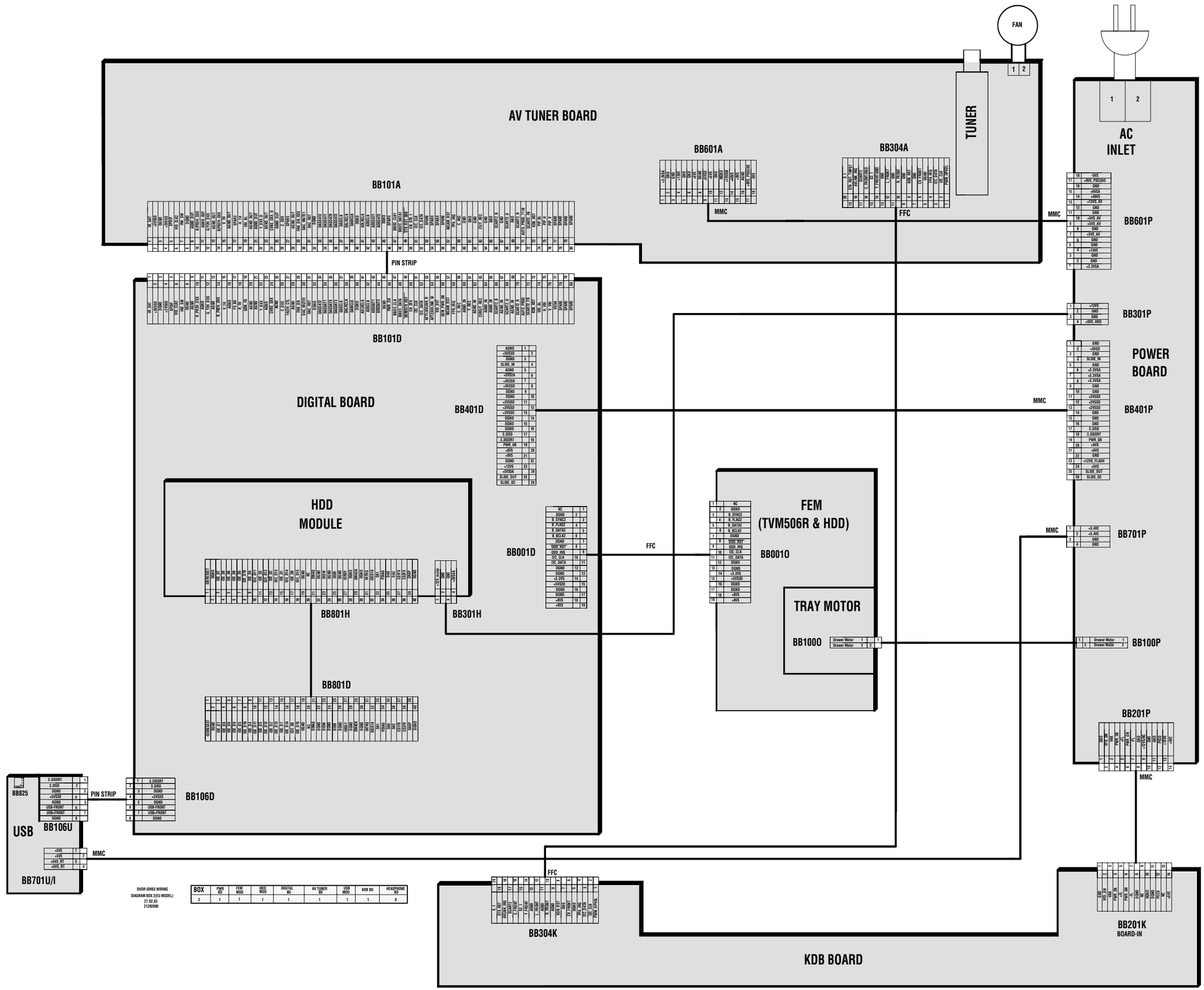


SOLDER SIDE - COTÉ CUIVRE - LÖTSEITE - LATO SALDATURE - LADO DEL COBRE



DTH7500 / DTH8000
First issue 12 / 03

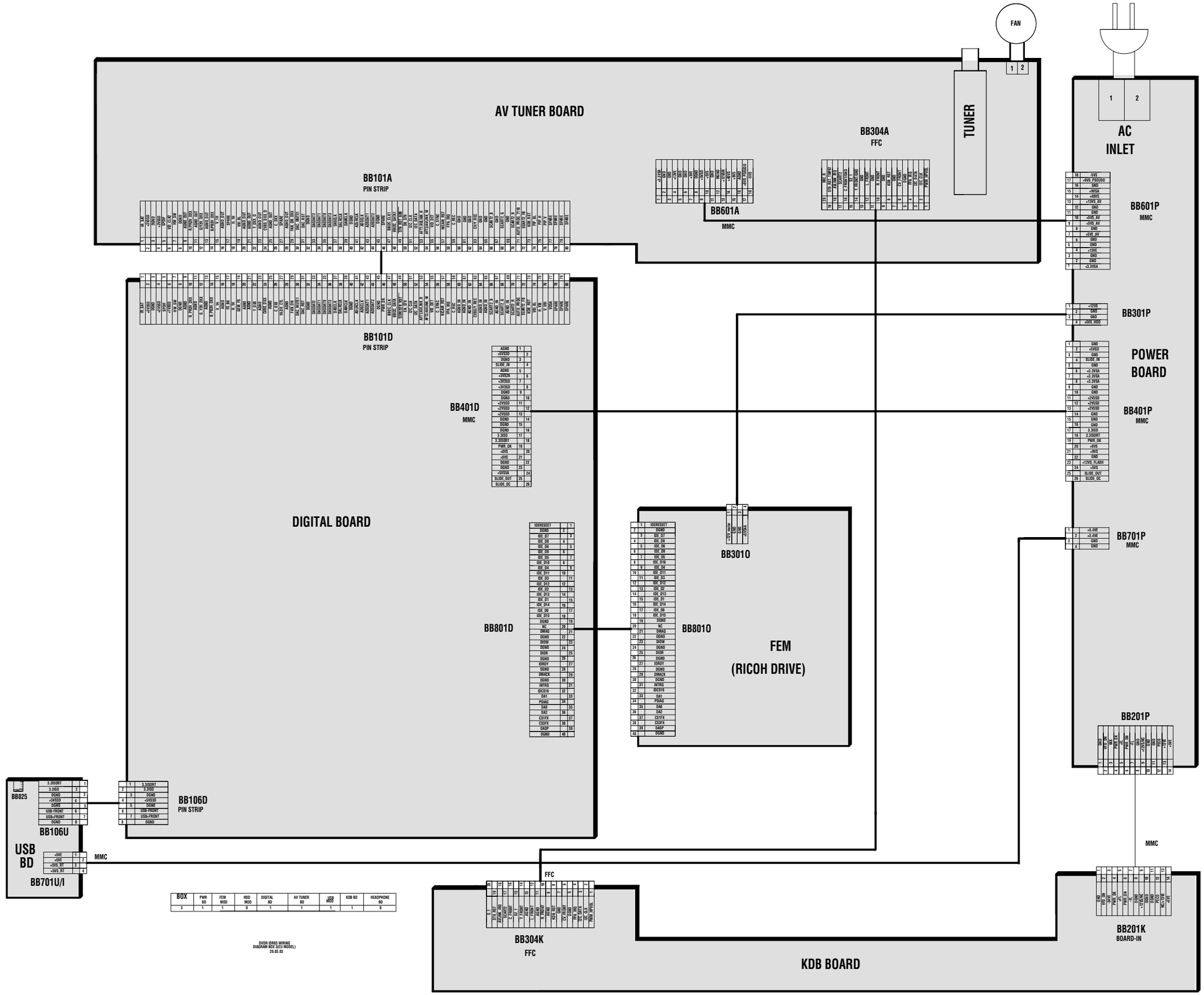
WIRING DIAGRAM - SCHEMA D'INTERCONNEXIONS - VERDRAHTUNGSPLAN - DIAGRAMMA DELLE INTERCONNESSIONI - ESQUEMA DE INTERCONEXIONES



DVDR IDR03 WIRING
DIAGRAM BOX 2(EU MODEL)
27.02.03
21230390

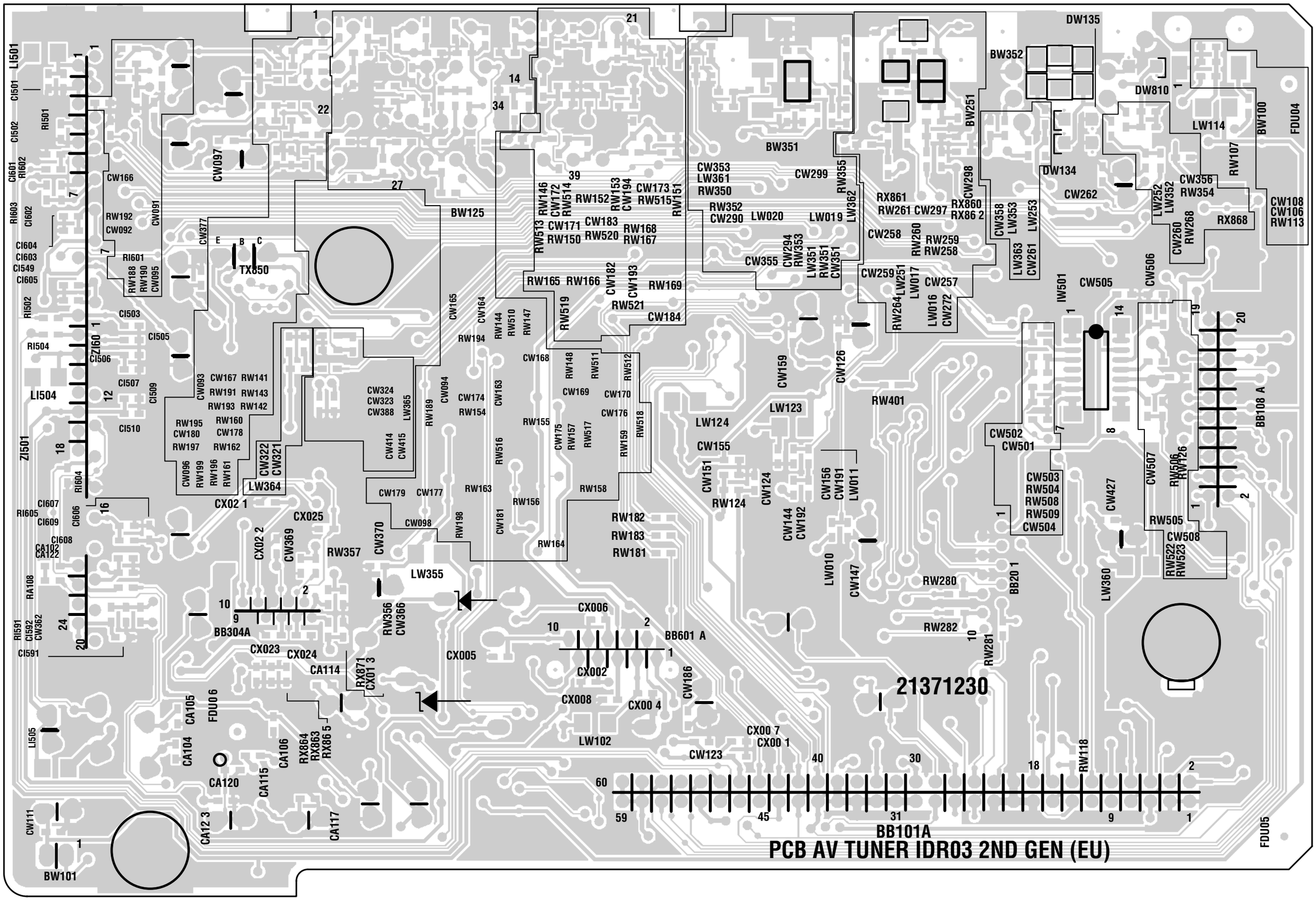
BOX	PWR BD	FEM MOD	HDD MOD	DIGITAL BD	AV TUNER BD	USB MOD	KDB BD	HEADPHONE BD
2	1	1	1	1	1	1	1	0

WIRING DIAGRAM - SCHEMA D'INTERCONNEXIONS - VERDRAHTUNGSPLAN - DIAGRAMMA DELLE INTERCONNESSIONI - ESQUEMA DE INTERCONEXIONES



SCART INTERFACE P.C.B./TUNER - PLATINE INTERFACE PERITELEVISION/TUNER -LEITERPLATTE EUROPA NORMBUCHSE/TUNER - PIASTRA PRESA PERITEL/TUNER PLATINA INTERFAZ EUROTOMA/TUNER

SOLDER SIDE - COTÉ CUIVRE - LÖTSEITE - LATO SALDATURE - LADO DEL COBRE



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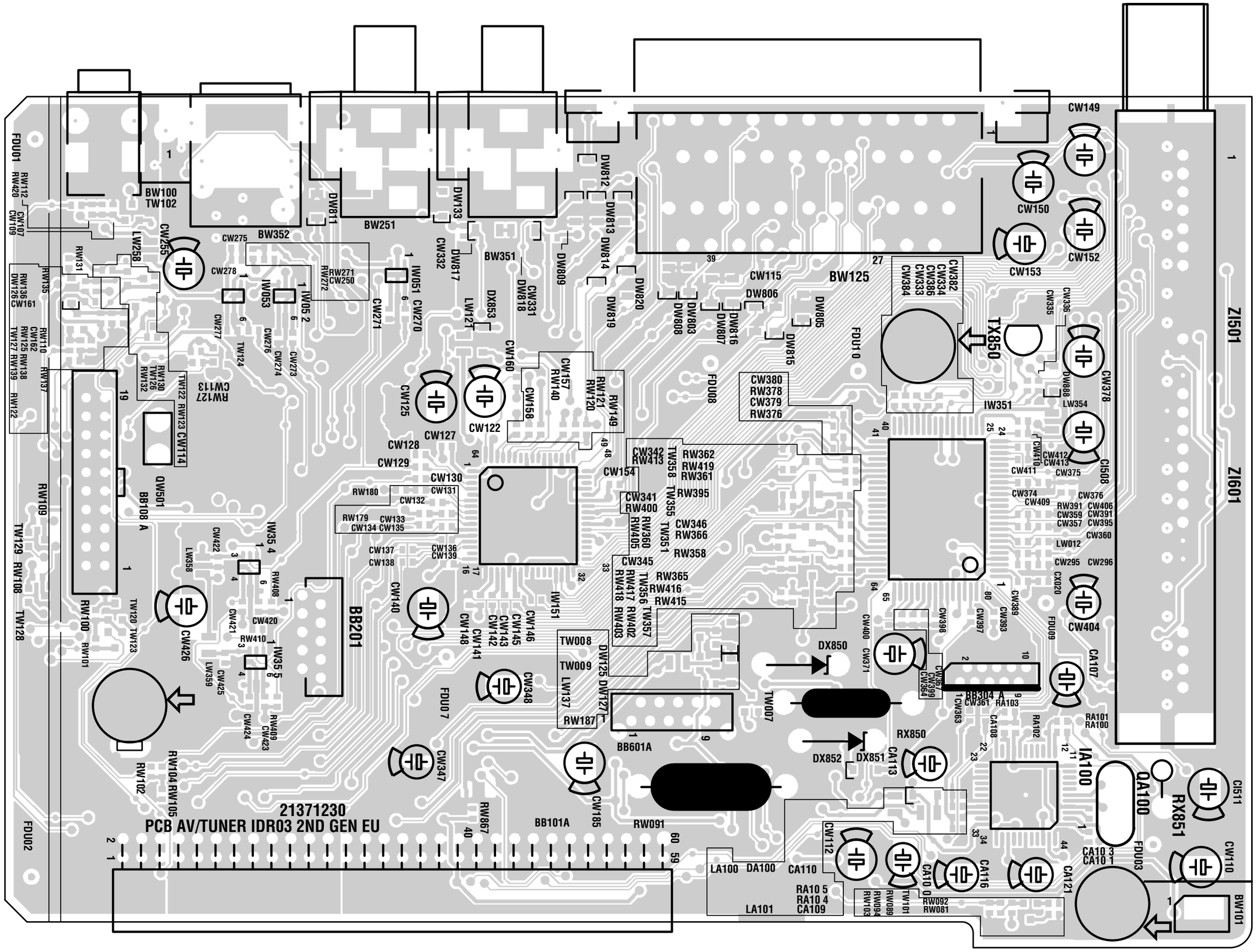
COMPONENT SIDE - COTÉ COMPOSANTS - BESTÜCKUNGSSEITE - LATO COMPONENTI - LADO COMPONENTES



DTH8000E(B)

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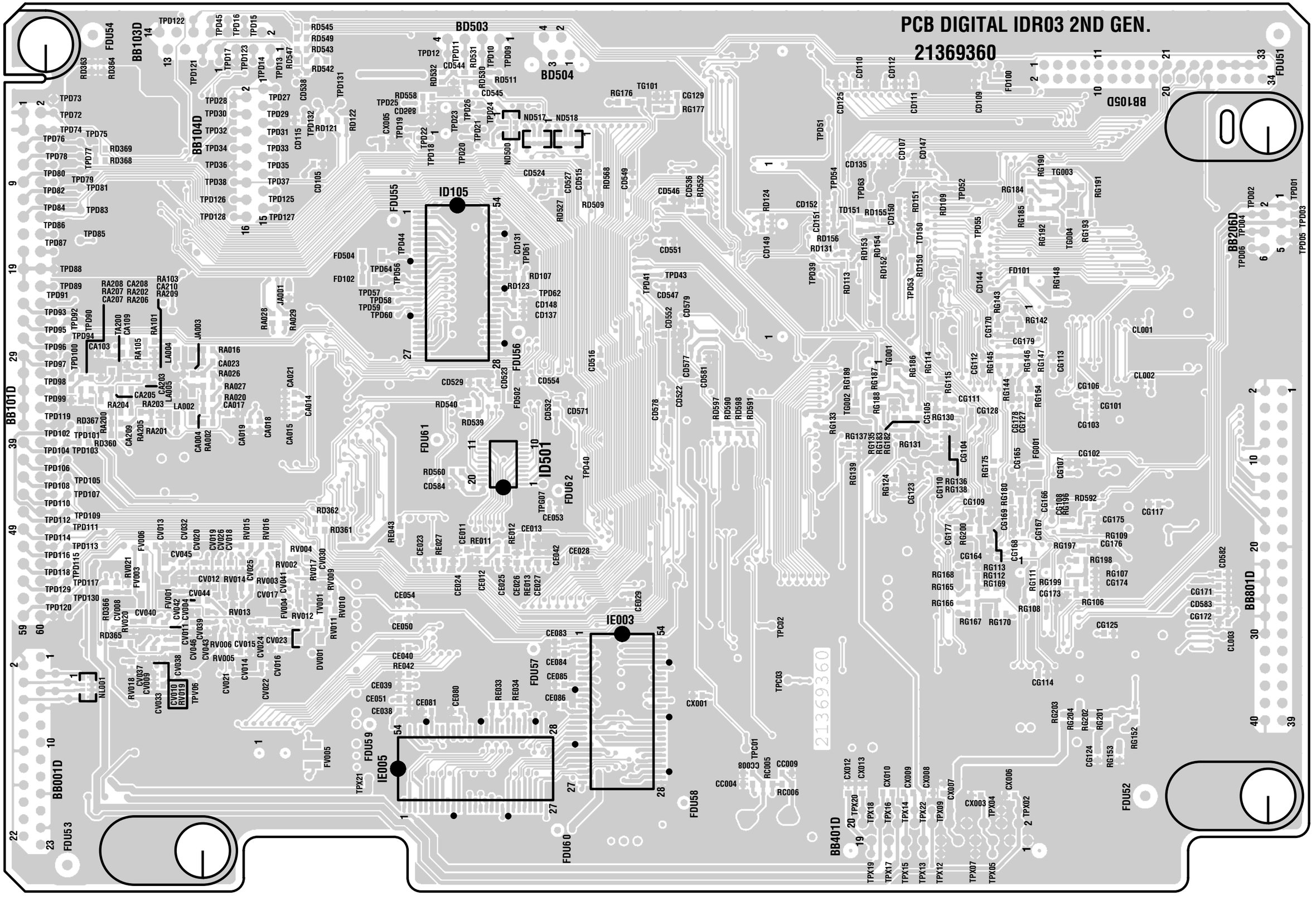
DIGITAL PROCESSING P.C.B. - PLATINE TRAITEMENT DIGITAL - LTPL. DIGITALVERARBEITUNG - PIASTRA ELABORAZIONE DIGITALE - PLATINA TRATAMIENTO DIGITAL

SOLDER SIDE - COTÉ CUIVRE - LÖTSEITE - LATO SALDATURE - LADO DEL COBRE



DTH8000E(B)

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DIGITAL PROCESSING P.C.B. - PLATINE TRAITEMENT DIGITAL - LTPL. DIGITALVERARBEITUNG - PIASTRA ELABORAZIONE DIGITALE - PLATINA TRATAMIENTO DIGITAL

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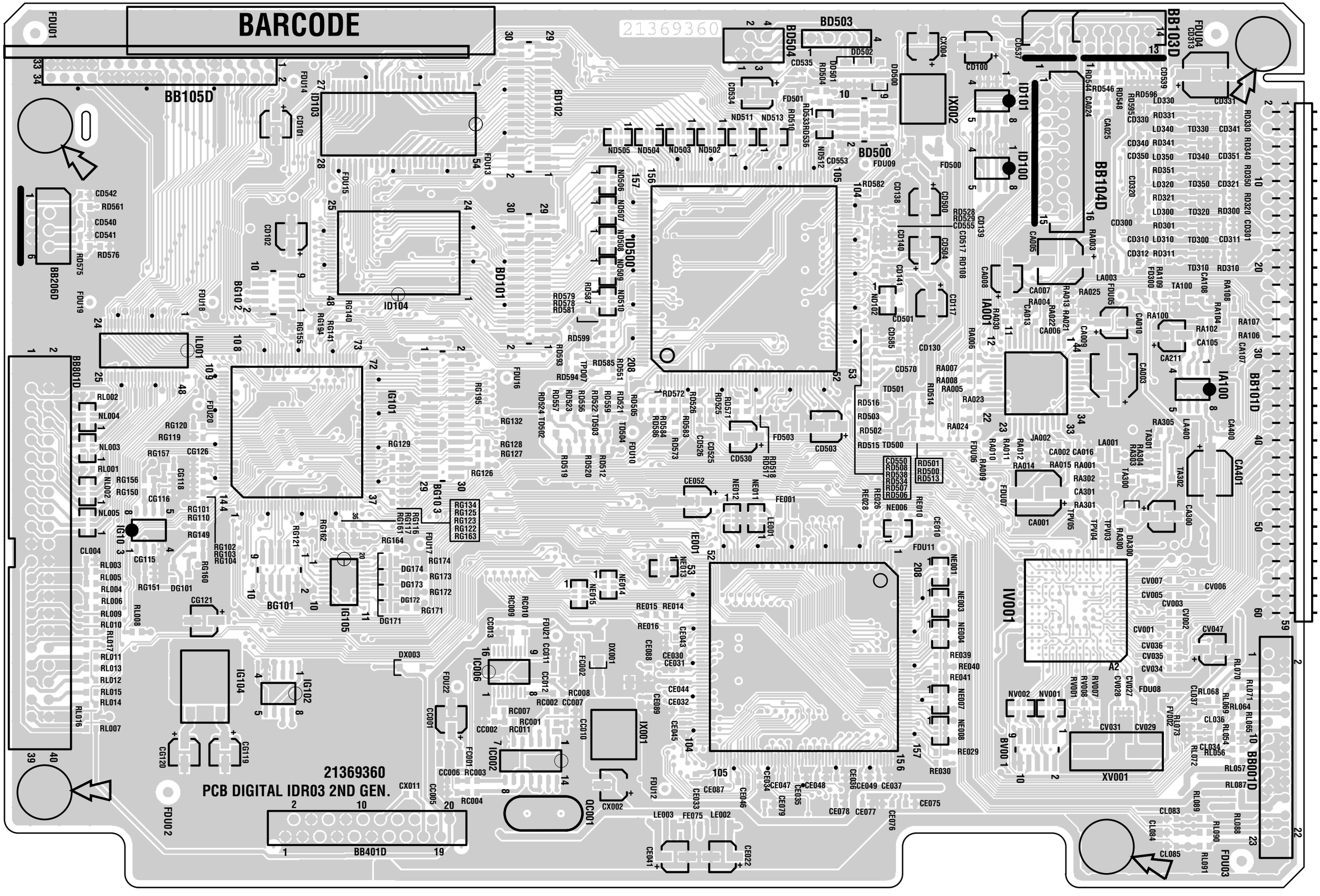
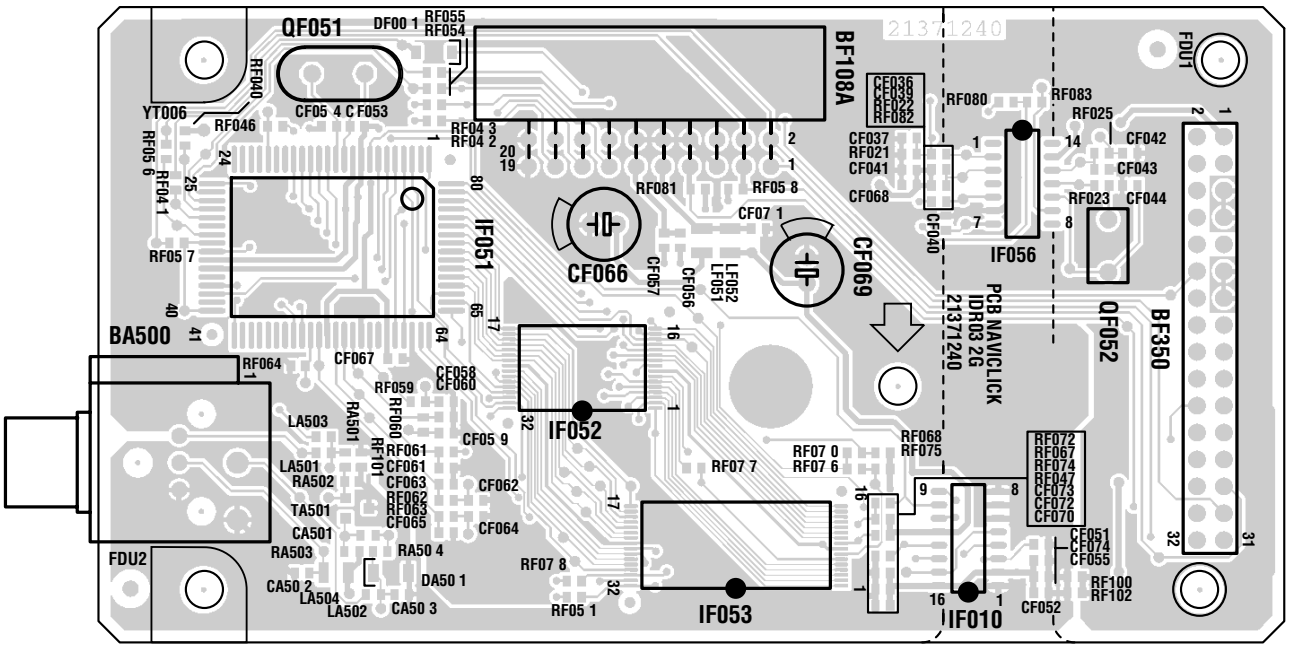
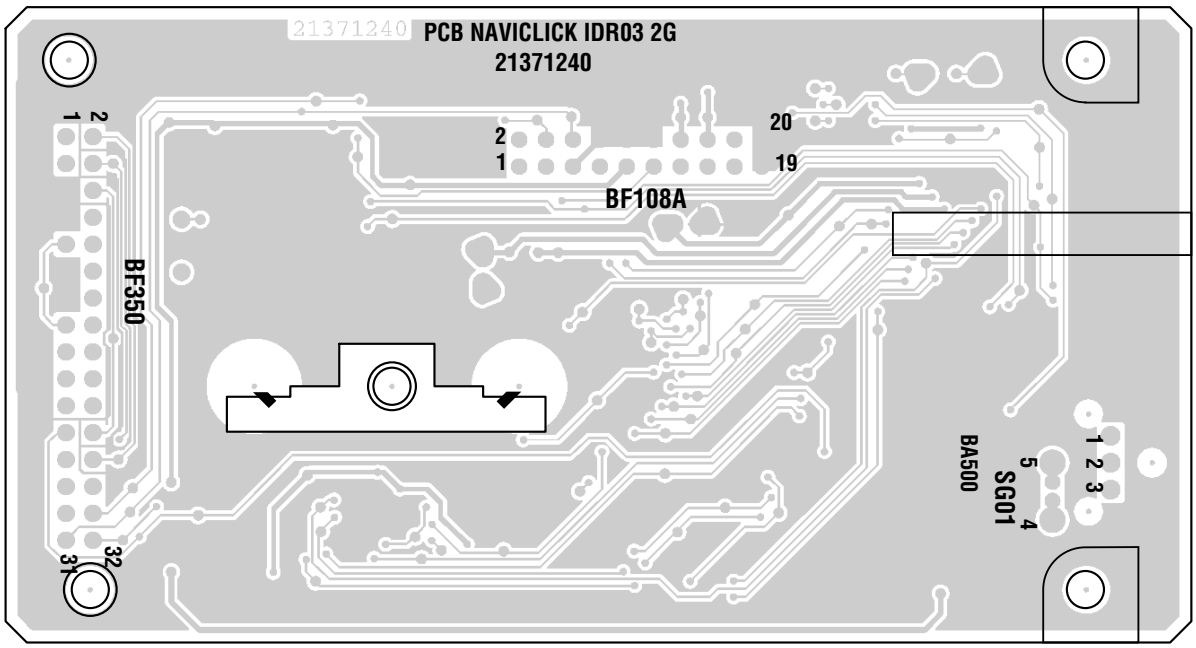


NAVICLICK MODULE - MODULE NAVICLICK - NAVICLICK MODUL
MODULO NAVICLICK - MÓDULO NAVICLICK

Component side - Côté composants - Bestückungsseite Lato componenti - Lado componentes

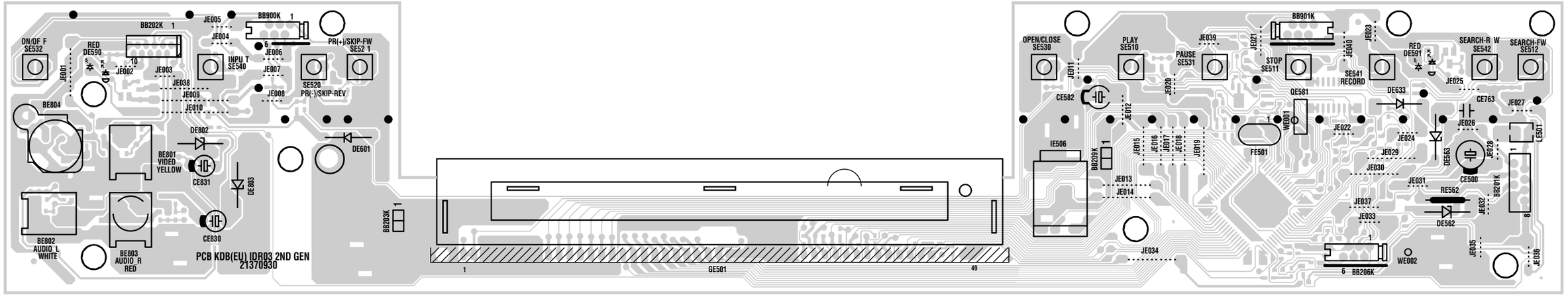


SOLDER SIDE - COTÉ CUIVRE - LÖTSEITE - LATO SALDATURE - LADO DEL COBRE

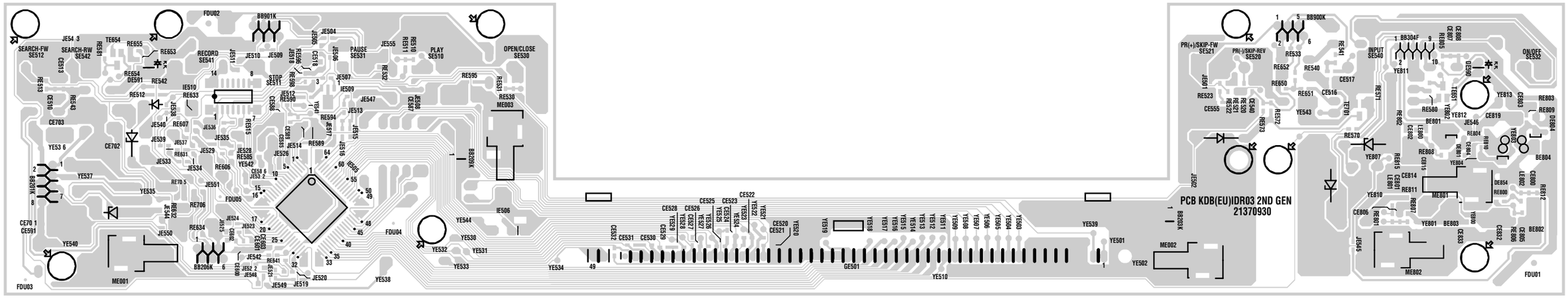


KEYBOARD CIRCUIT BOARDS - CIRCUITS IMPRIMES PLATINES COMMANDES - LEITERPLATTE BEDIENTEIL - PIASTRE TASTIERA - PLATINAS MANDOS

COMPONENT SIDE - COTÉ COMPOSANTS - BESTÜCKUNGSSEITE - LATO COMPONENTI - LADO COMPONENTES

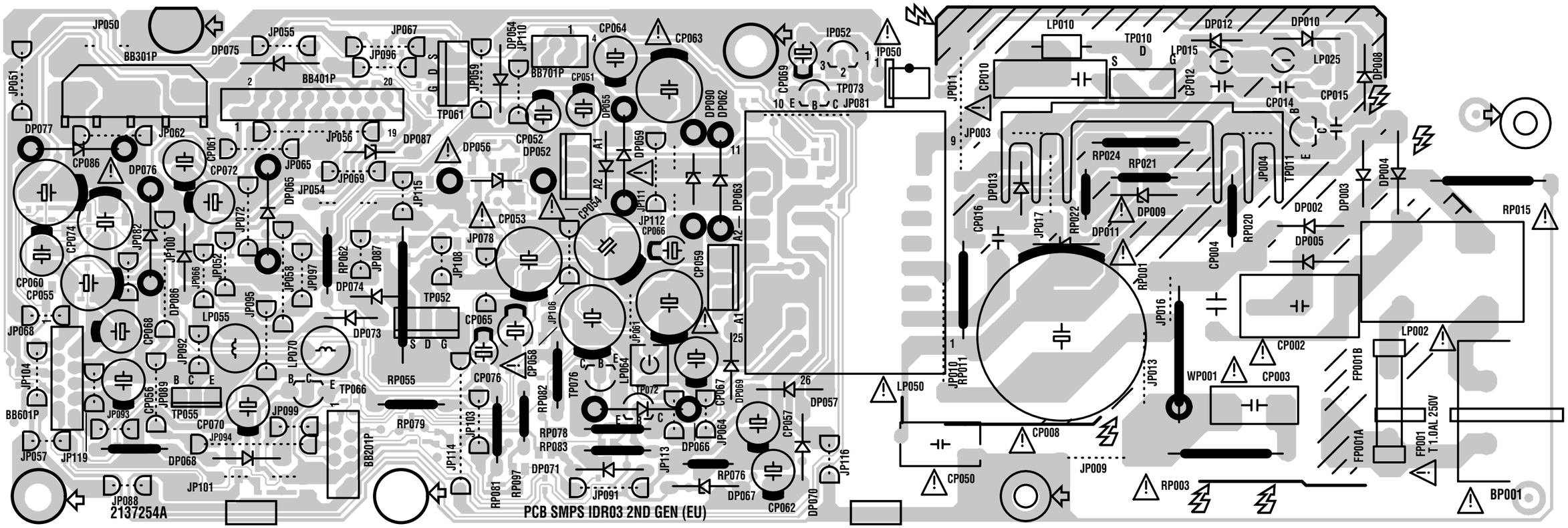


SOLDER SIDE - COTÉ CUIVRE - LÖTSEITE - LATO SALDATURE - LADO DEL COBRE



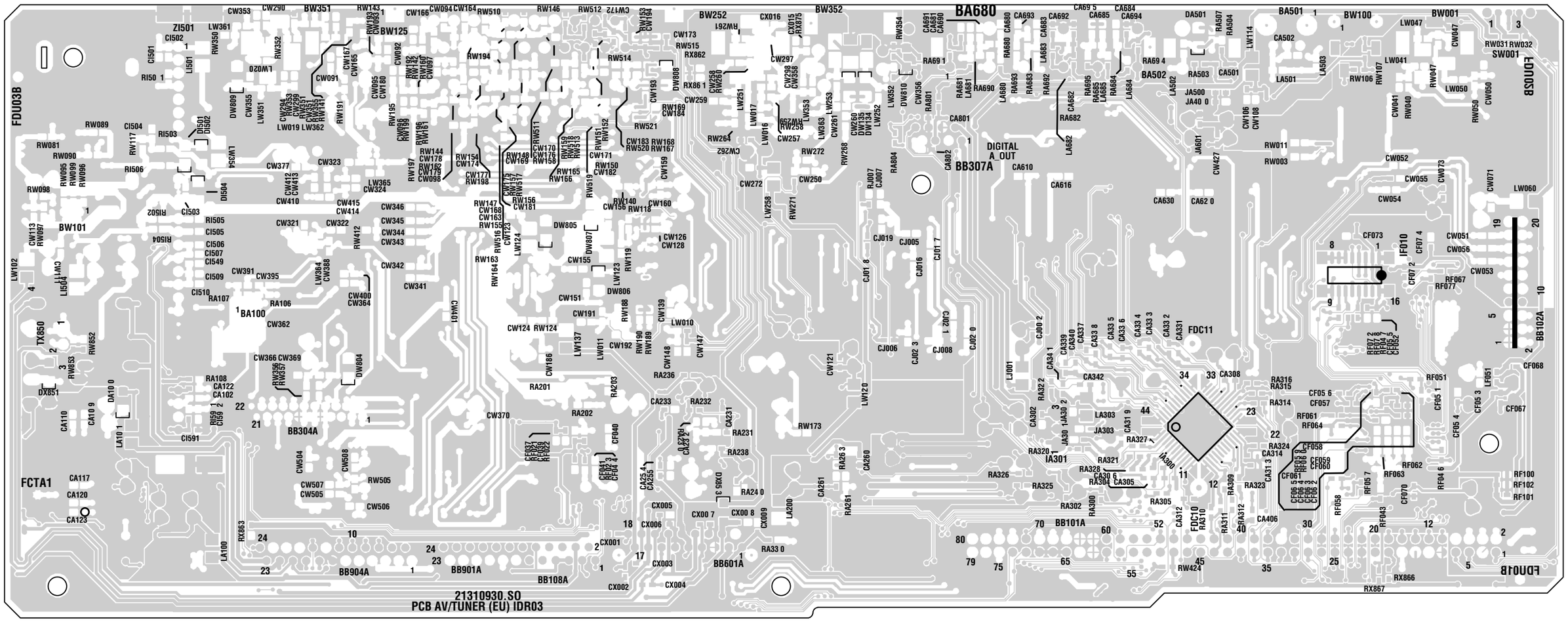
POWER SUPPLY CIRCUIT BOARD - CIRCUIT IMPRIME DE L'ALIMENTATION - LEITERPLATTE NETZTEIL - PIASTRA DEI CIRCUITI DI ALIMENTAZIONE - PLATINA ALIMENTACIÓN

COMPONENT SIDE - COTÉ COMPOSANTS - BESTÜCKUNGSSEITE - LATO COMPONENTI - LADO COMPONENTES



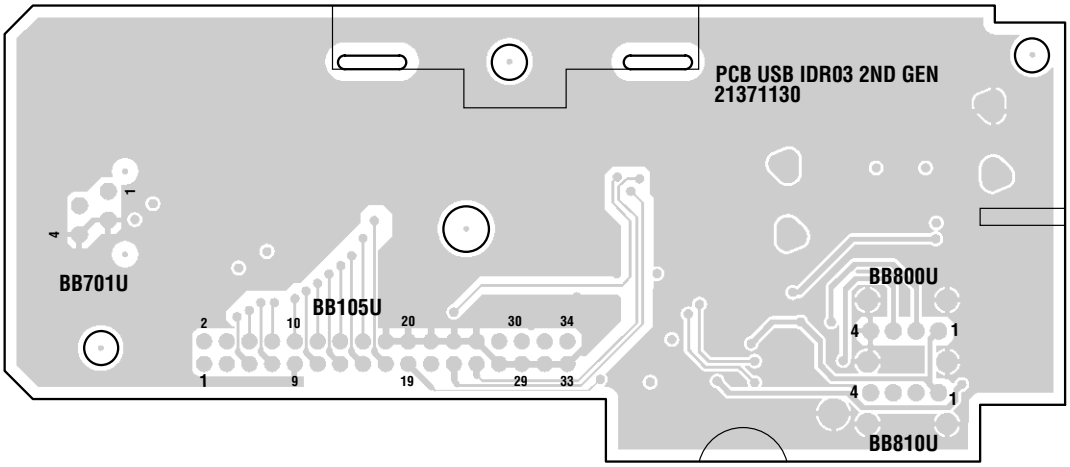
SCART INTERFACE P.C.B./TUNER - PLATINE INTERFACE PERITELEVISION/TUNER -LEITERPLATTE EUROPA NORMBUCHSE/TUNER - PIASTRA PRESA PERITEL/TUNER PLATINA INTERFAZ EUROTOMA/TUNER

SOLDER SIDE - COTÉ CUIVRE - LÖTSEITE - LATO SALDATURE - LADO DEL COBRE

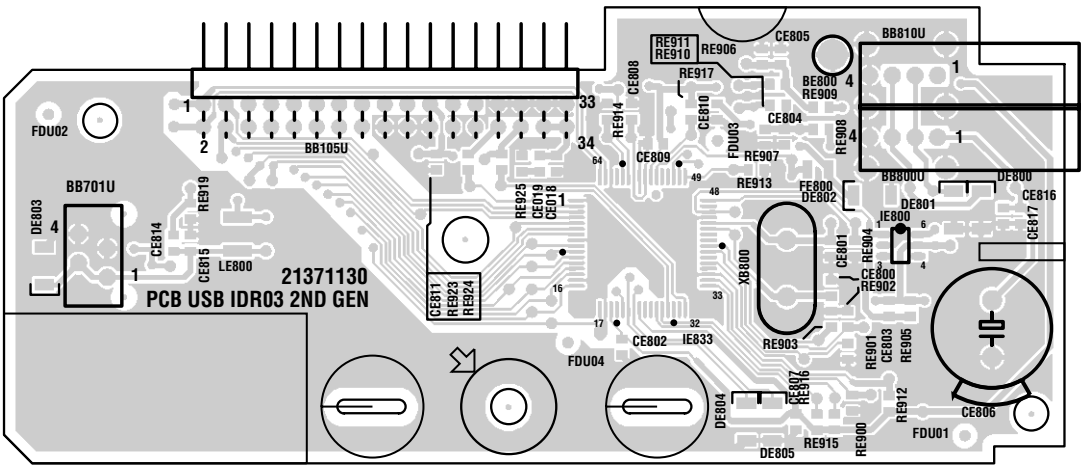


USB MODULE - MODULE USB - USB MODUL - MODULO USB - MÓDULO USB

SOLDER SIDE - COTÉ CUIVRE - LÖTSEITE - LATO SALDATURE
LADO DEL COBRE



COMPONENT SIDE - COTÉ COMPOSANTS - BESTÜCKUNGSSEITE
LATO COMPONENTI - LADO COMPONENTES



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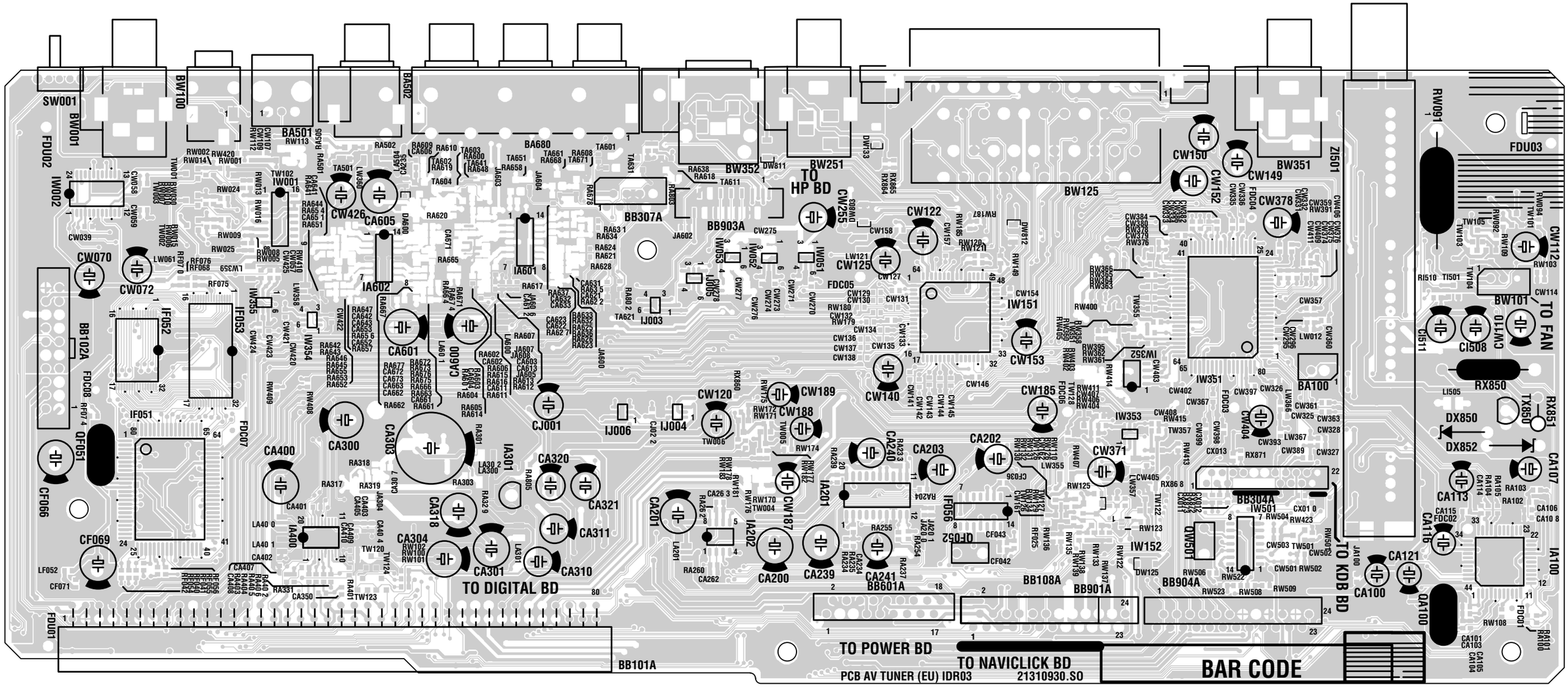

DIGITAL PROCESSING P.C.B. - PLATINE TRAITEMENT DIGITAL - LTPL. DIGITALVERARBEITUNG - PIASTRA ELABORAZIONE DIGITALE - PLATINA TRATAMIENTO DIGITAL

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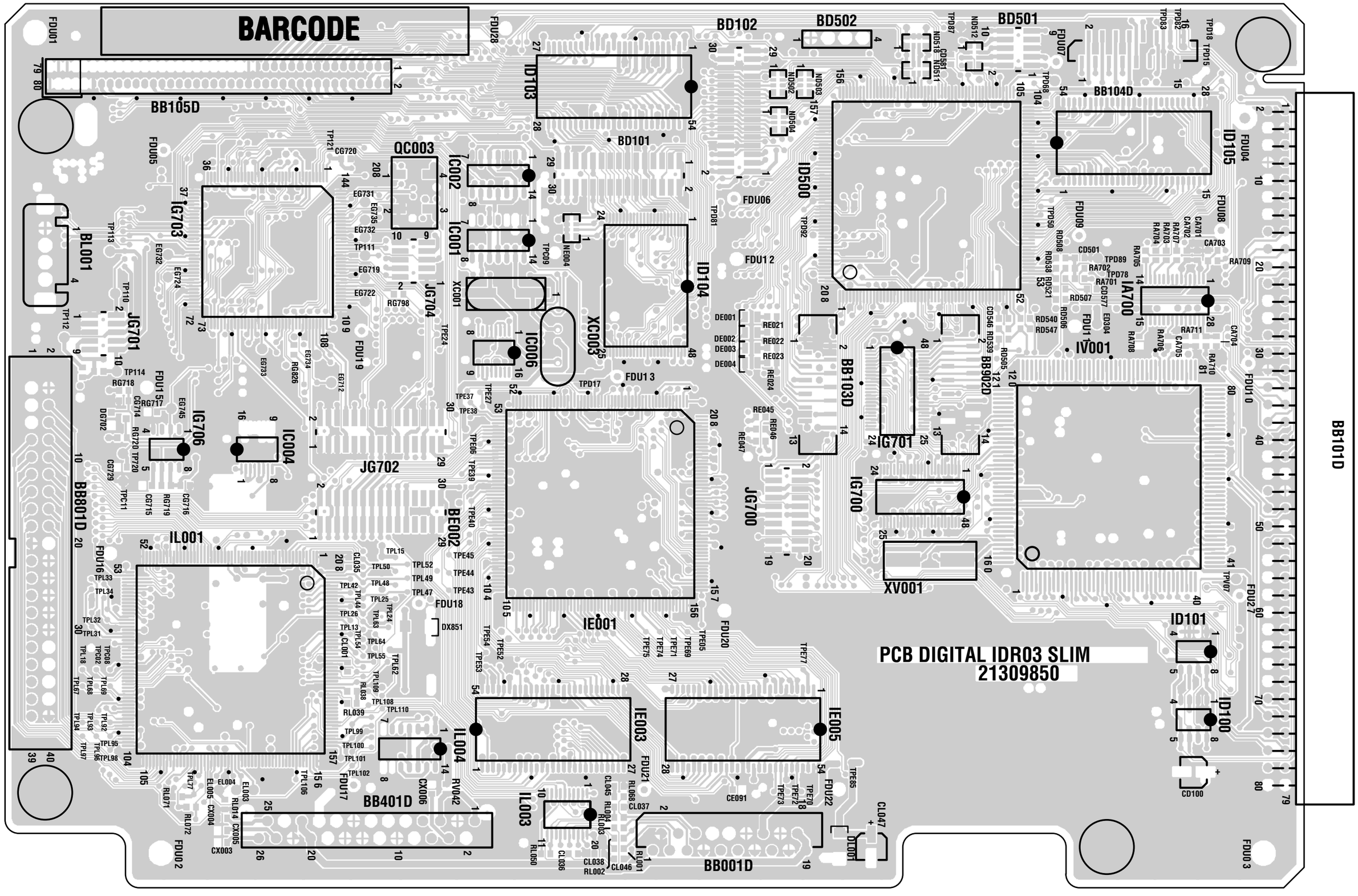

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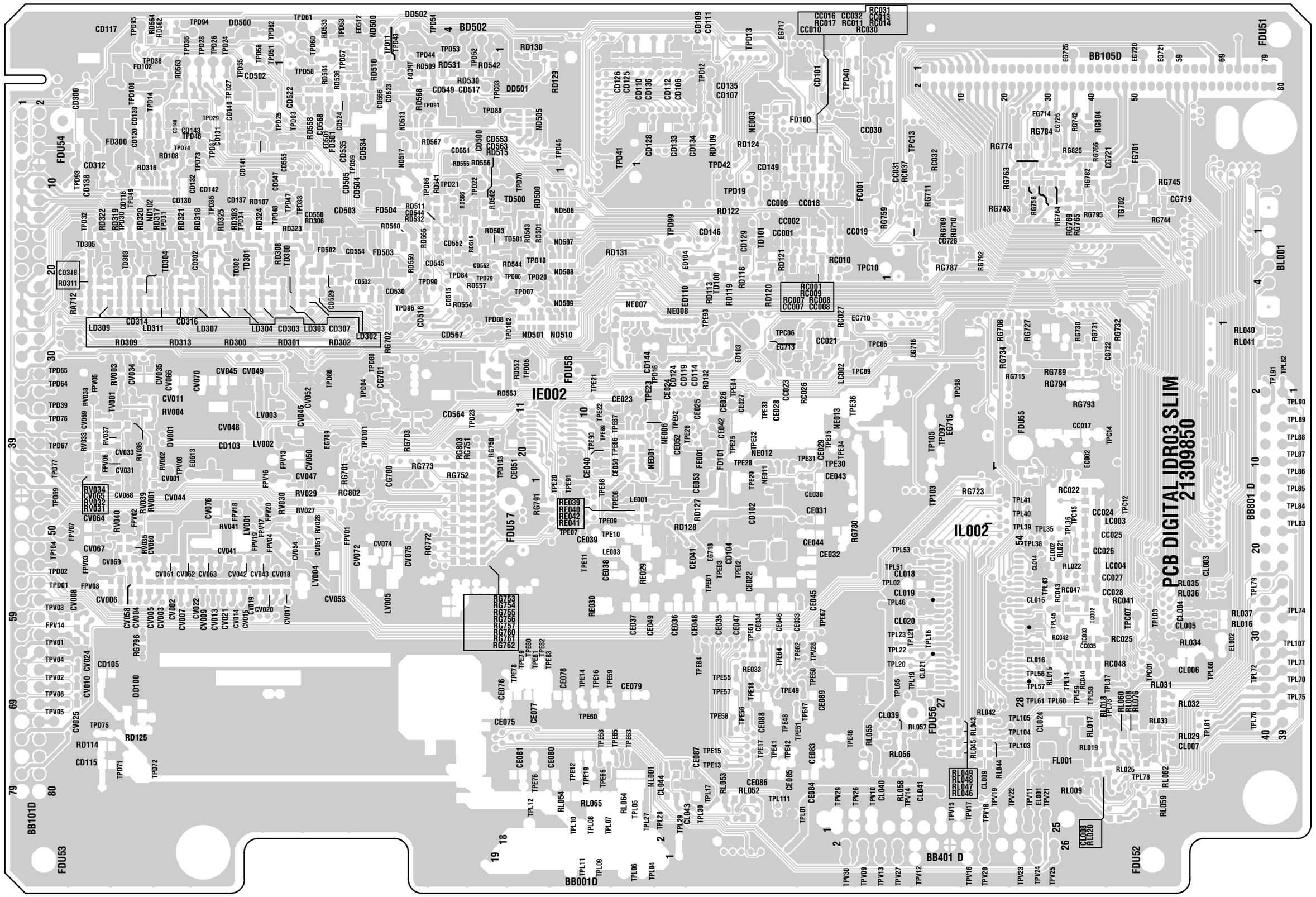

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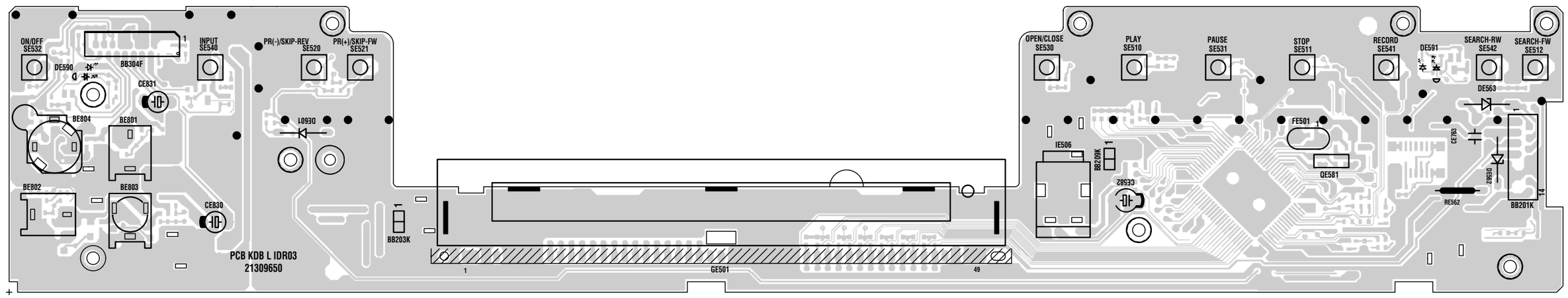
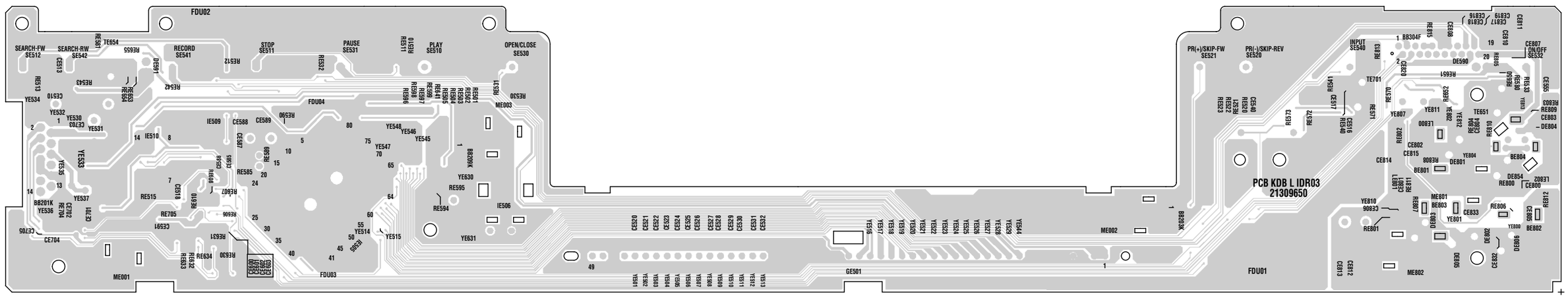


SOLDER SIDE - COTÉ CUIVRE - LÖTSEITE - LATO SALDATURE - LADO DEL COBRE



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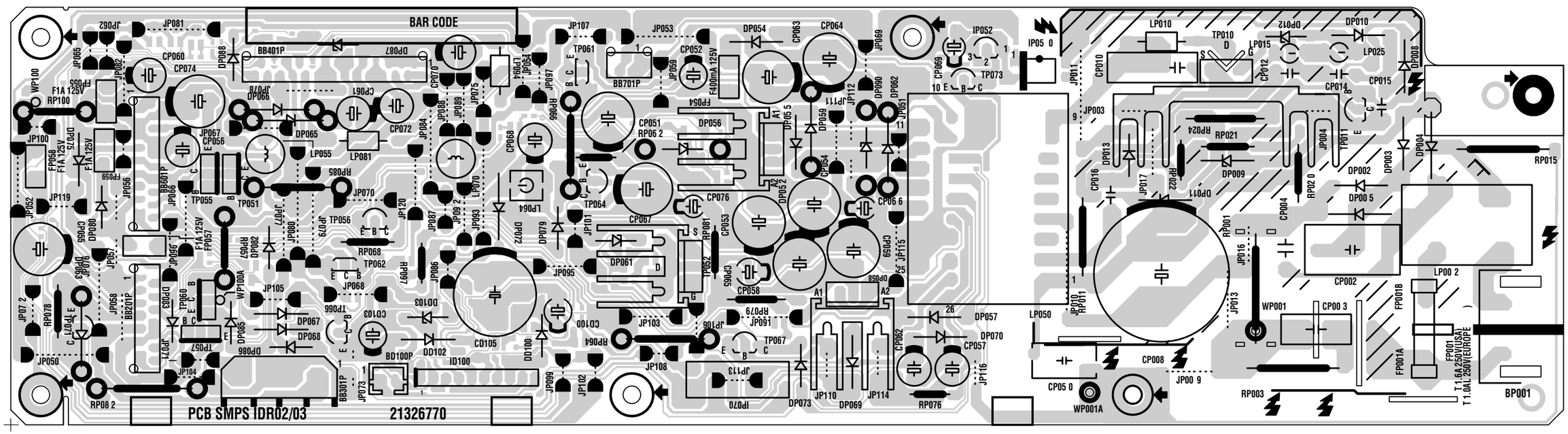

SOLDER SIDE - COTÉ CUIVRE - LÖTSEITE - LATO SALDATURE - LADO DEL COBRE